1.1GHz Low Power Two-Modulus Prescaler With On-Chip Output Termination

The MC12022TSA can be used with CMOS synthesizers requiring positive edges to trigger internal counters such as Motorola's MC145XXX series in a PLL to provide tuning signals up to 1.1GHz in programmable frequency steps. This device is a reduced current drain version of the MC12022A/B with the addition of on-chip output termination.

The MC12022TSB can be used with CMOS synthesizers requiring negative edges to trigger internal counters.

A Divide Ratio Control (SW) permits selection of a 64/65 or 128/129 divide ratio as desired.

The Modulus Control (MC) selects the proper divide number after SW has been biased to select the desired divide ratio.

- 1.1 GHz Toggle Frequency
- Supply Voltage of 4.5 to 5.5V
- Low-Power 4.0mA Typical
- Operating Temperature Range of –40 to +85°C
- Short Setup Time (tset) 16ns Maximum @ 1.1GHz
- Modulus Control Input Level Is Compatible With Standard CMOS and TTL
- Output Load Resistor on Die

FUNCTIONAL TABLE

SW	MC	Divide Ratio
н	Н	64
Н	L	65
L	Н	128
L	L	129

Note: SW: H = V_{CC}, L = Open

MC: H = 2.0 V to V_{CC} , L = GND to 0.8 V

DESIGN GUIDE

Criteria	Value	Unit	
Internal Gate Count*	67	ea	
Internal Gate Propagation Delay	200	ps	
Internal Gate Power Dissipation	0.75	mW	
Speed Power Product	0.15	рJ	

* Equivalent to a two-input NAND gate

MAXIMUM RATINGS

Symbol	Characteristic	Range	Unit
VCC	Power Supply Voltage, Pin 2	-0.5 to + 7.0	Vdc
т _А	Operating Temperature Range	-40 to + 85	°C
T _{stg}	Storage Temperature Range	-65 to + 150	°C



MECL PLL COMPONENTS

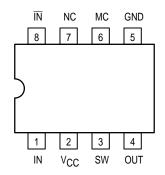
÷64/65, ÷128/129 TWO-MODULUS PRESCALER





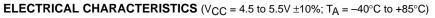
D SUFFIX PLASTIC SOIC PACKAGE CASE 751-05

Pinout: 8-Lead Plastic (Top View)



MC12022TSA MC12022TSB

Symbol	Characteristic	Min	Тур	Max	Unit
ft	Toggle Frequency (Sine Wave Input)	0.1	1.4	1.1	GHz
ICC	Supply Current Output Unloaded (Pin 2) at 2.7Vdc		4.0	6.5	mA
VIH1	Modulus Control Input High (MC)	2.0			V
VIL1	Modulus Control Input Low (MC)			0.8	V
V _{IH2}	Divide Ratio Control Input High (SW)	$V_{CC} - 0.5V$	VCC	V _{CC} – 0.5V	Vdc
V _{IL2}	Divide Ratio Control Input Low (SW)	Open	Open	Open	
V _{out}	Output Voltage Swing (C _L = 8pF; R _L = $4.4k\Omega$)	1.0	1.4		V _{p-p}
t _{set}	Modulus Setup Time MC to Out		11	16	ns
V _{in}	Input Voltage Sensitivity 250–1100 MHz 100–250 MHz	100 400		1500 1500	mVpp



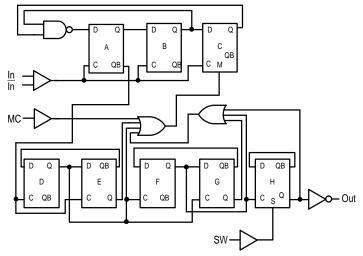
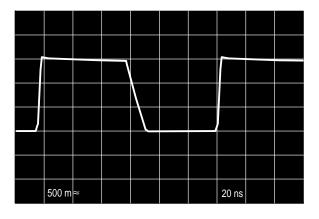
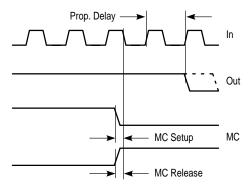


Figure 1. Logic Diagram (MC12022TSA)

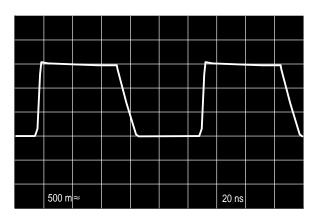


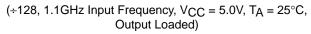
(+64, 500MHz Input Frequency, V_{CC} = 5.0V, T_A = 25°C, Output Loaded)



Modulus setup time MC to out is the MC setup or MC release plus the prop delay.

Figure 2. Modulus Setup Time







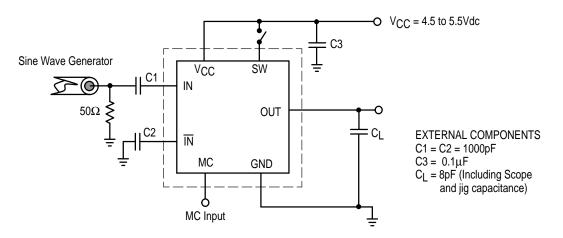
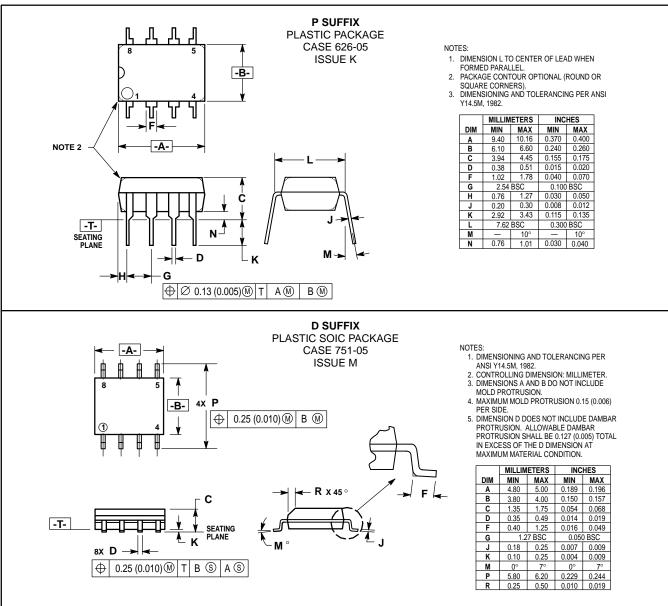


Figure 4. AC Test Circuit

MC12022TSA MC12022TSB

OUTLINE DIMENSIONS



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MC12022TSA/D



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