# **Quad Differential AND/NAND**

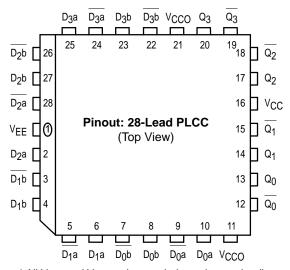
The MC10E404/100E404 is a 4-bit differential AND/NAND device. The differential operation of the device makes it ideal for pulse shaping applications where duty cycle skew is critical. Special design techniques were incorporated to minimize the skew between the upper and lower level gate inputs.

Because a negative 2-input NAND function is equivalent to a 2-input OR function, the differential inputs and outputs of the device also allow for its use as a fully differential 2 input OR/NOR function.

The output RISE/FALL times of this device are significantly faster than most other standard ECLinPS devices resulting in an increased bandwidth.

The differential inputs have clamp structures which will force the Q output of a gate in an open input condition to go to a LOW state. Thus, inputs of unused gates can be left open and will not affect the operation of the rest of the device. Note that the input clamp will take affect only if both inputs fall 2.5V below  $V_{CC}$ .

- Differential D and Q
- 700ps Max. Propagation Delay
- High Frequency Outputs
- Extended 100E VFF Range of 4.2V to 5.46V
- Internal 75kΩ Input Pulldown Resistors



 $^{\ast}$  All VCC and VCCO pins are tied together on the die.

## **PIN NAMES**

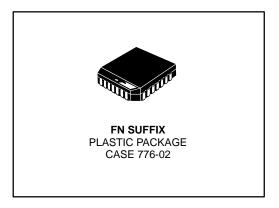
Pin	Function
D[0:4], D[0:4]	Differential Data Inputs
Q[0:4], Q[0:4]	Differential Data Outputs

#### **FUNCTION TABLE**

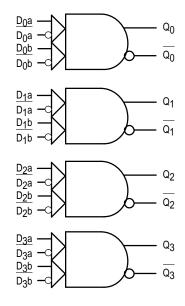
Da	Db	Q	Da	Db	Q
L	L	L	L	L	L
L	Н	L	L	Н	Н
Н	L	L	Н	L	Н
Н	Н	Н	Н	Н	Н

# MC10E404 MC100E404

# QUAD DIFFERENTIAL AND/NAND



#### **LOGIC DIAGRAM**





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# MC10E404 MC100E404

# DC CHARACTERISTICS (VEE = VEE(min) to VEE(max); VCC = VCCO = GND)

			0°C			25°C			85°C			
Symbol	Characteristic	min	typ	max	min	typ	max	min	typ	max	Unit	Condition
lН	Input HIGH Current			150			150			150	μΑ	
IEE	Power Supply Current										mA	
	10E		106	127		106	127		106	127		
	100E		106	127		106	127		122	146		
V <sub>PP</sub> (DC)	Input Sensitivity	50			50			50			mV	1
VCMR	Common Mode Range	-1.5		0	-1.5		0	-1.5		0	V	2

# AC CHARACTERISTICS (VEE= VEE(min) to VEE(max); VCC = VCCO = GND)

			0°C			25°C			85°C			
Symbol	Characteristic	min	typ	max	min	typ	max	min	typ	max	Unit	Condition
tPLH	Propagation Delay to Output										ps	
tPHL	Da (Diff)	350	475	650	350	475	650	350	475	650		
	Da (SE)	300	475	700	300	475	700	300	475	700		
	Db (Diff)	375	500	675	375	500	675	375	500	675		
	Db (SE)	325	500	725	325	500	725	325	500	725		
<sup>t</sup> SKEW	Within-Device Skew		50			50			50		ps	1
V <sub>PP</sub> (AC)	Minimum Input Swing	150			150			150			mV	2
t <sub>r</sub>	Rise/Fall Time											
tf	20 - 80%	150		400	150		400	150		400	ps	

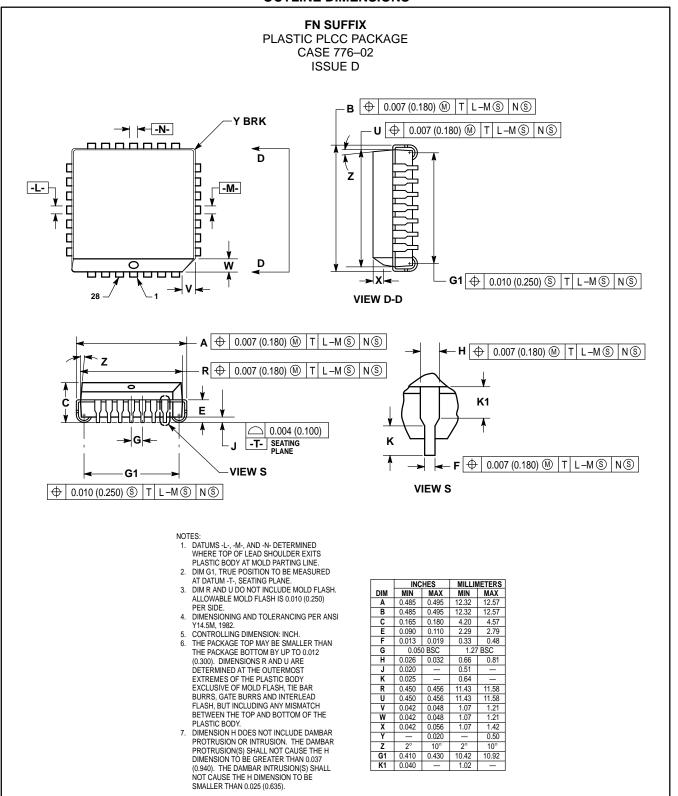
<sup>1.</sup> Within-device skew is defined as identical transitions on similar paths through a device.

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Differential input voltage required to obtain a full ECL swing on the outputs.
V<sub>CMR</sub> is referenced to the most positive side of the differential input signal. Normal operation is obtained when the input signals are within the V<sub>CMR</sub> range and the input swing is greater than V<sub>PP MIN</sub> and < 1.0V.</li>

<sup>2.</sup> Minimum input swing for which AC parameters are guaranteed.

## **OUTLINE DIMENSIONS**



### MC10E404 MC100E404

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