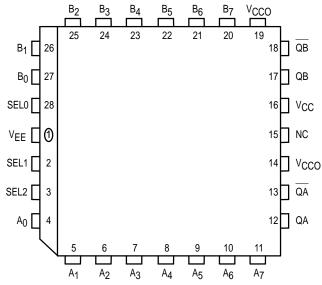
2-Bit 8:1 Multiplexer

The MC10E/100E163 contains two 8:1 multiplexers with differential outputs and common select inputs. The select inputs (SEL0, 1, 2) control which one of the eight data inputs ($A_0 - A_7$, $B_0 - B_7$) is propagated to the output.

- 850ps Max. D to Output
- Differential Outputs
- Extended 100E V_{EE} Range of − 4.2V to − 5.46V
- 75kΩ Input Pulldown Resistors

Pinout: 28-Lead PLCC (Top View)



 * All VCC and VCCO pins are tied together on the die.

FUNCTION TABLE

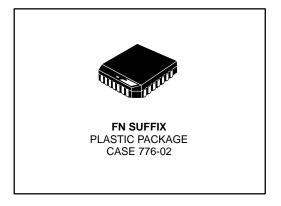
| SEL2 | SEL1 | SEL0 | A/B Data |
|------|------|------|-------------|
| L | L | L | 0 |
| L | L | Н | 1 |
| L | Н | L | 2 |
| L | Н | Н | 3 |
| Н | L | L | 4 |
| Н | L | Н | 5 |
| Н | Н | L | 6 |
| Н | Н | Н | 7 |

PIN NAMES

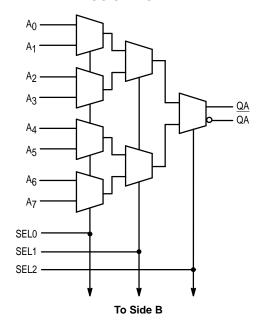
| Pin | Function |
|---------------------------------|-------------------|
| A ₀ – A ₇ | A Data Inputs |
| B ₀ – B ₇ | B Data Inputs |
| SEL0, 1, 2 | Select Inputs |
| QA, QB | True Outputs |
| QA, QB | Inverting Outputs |

MC10E163 MC100E163

2-BIT 8:1 MULTIPLEXER



LOGIC DIAGRAM





12/93

REV 2

DC CHARACTERISTICS (VEE = VEE(min) to VEE(max); VCC = VCCO = GND)

| | | 0°C | | 25°C | | | 85°C | | | | | |
|--------|----------------------|-----|-----|------|-----|-----|------|-----|-----|-----|------|-----------|
| Symbol | Characteristic | min | typ | max | min | typ | max | min | typ | max | Unit | Condition |
| lН | Input HIGH Current | | | 150 | | | 150 | | | 150 | μΑ | |
| IEE | Power Supply Current | | | | | | | | | | mΑ | |
| | 10E | | 73 | 88 | | 73 | 88 | | 73 | 88 | | |
| | 100E | | 73 | 88 | | 73 | 88 | | 83 | 100 | | |

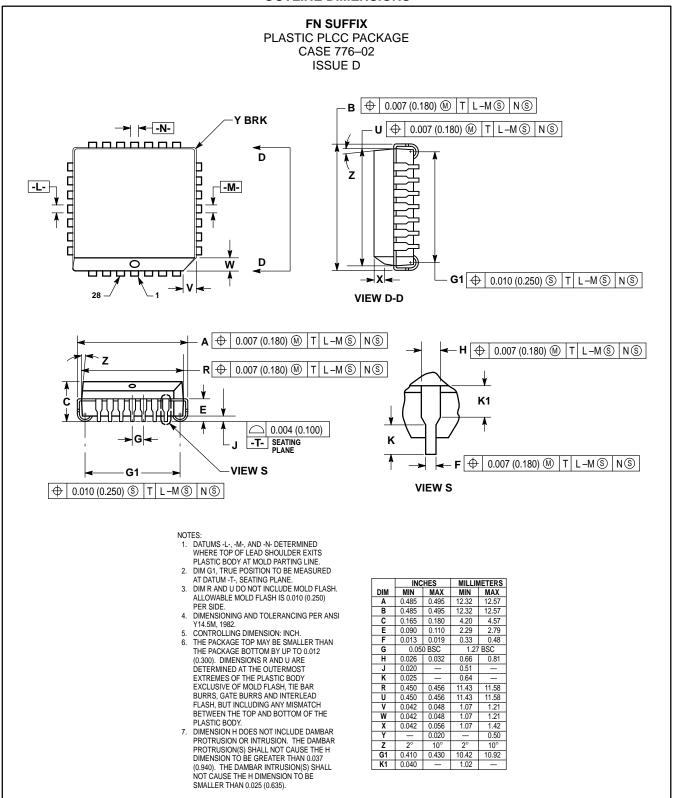
AC CHARACTERISTICS ($V_{EE} = V_{EE}(min)$ to $V_{EE}(max)$; $V_{CC} = V_{CCO} = GND$)

| | | 0°C | | 25°C | | | 85°C | | | 1 | | |
|----------------|-----------------------------|-----|-----|------|-----|-----|------|-----|-----|-----|------|-----------|
| Symbol | Characteristic | min | typ | max | min | typ | max | min | typ | max | Unit | Condition |
| tPLH | Propagation Delay to Output | | | | | | | | | | | ps |
| tPHL | D | 400 | 550 | 800 | 400 | 550 | 800 | 400 | 550 | 800 | | |
| | SEL0 | 525 | 725 | 950 | 525 | 725 | 950 | 525 | 725 | 950 | l | |
| | SEL1 | 425 | 625 | 850 | 425 | 625 | 850 | 425 | 625 | 850 | | |
| | SEL2 | 350 | 525 | 725 | 350 | 525 | 725 | 350 | 525 | 725 | | |
| tSKEW | Within-Device Skew | | | | | | | | | | ps | 1 |
| | An, Bn to Q | 1 | 40 | | | 40 | | İ | 40 | | İ | |
| | An, Am to QA | 1 | 30 | | | 30 | | | 30 | | | |
| | Bn, Bm to QB | | 30 | | | 30 | | | 30 | | | |
| t _r | Rise/Fall Time | | | | | | | | | | ps | |
| t _f | 20 - 80% | 275 | 375 | 575 | 275 | 375 | 575 | 275 | 375 | 575 | | |

^{1.} Within-device skew is defined as identical transitions on similar paths through a device; n = 0-7, m n, m = 0-7.

MOTOROLA 2–2

OUTLINE DIMENSIONS



MC10E163 MC100E163

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How to reach us:

USA/EUROPE/Locations Not Listed: Motorola Literature Distribution; P.O. Box 20912; Phoenix, Arizona 85036. 1–800–441–2447 or 602–303–5454

MFAX: RMFAX0@email.sps.mot.com - TOUCHTONE 602-244-6609 INTERNET: http://Design-NET.com

JAPAN: Nippon Motorola Ltd.; Tatsumi–SPD–JLDC, 6F Seibu–Butsuryu–Center, 3–14–2 Tatsumi Koto–Ku, Tokyo 135, Japan. 03–81–3521–8315

ASIA/PACIFIC: Motorola Semiconductors H.K. Ltd.; 8B Tai Ping Industrial Park, 51 Ting Kok Road, Tai Po, N.T., Hong Kong. 852–26629298



