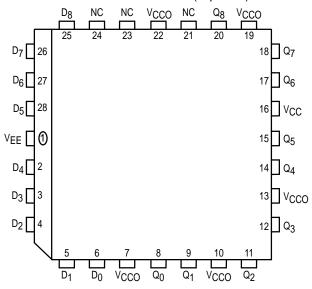
# 9-Bit Buffer

The MC10E/100E122 is a 9-bit buffer. The device contains nine non-inverting buffer gates.

- 500ps Max. Propagation Delay
- Extended 100E VEE Range of 4.2V to 5.46 V
- 75kΩ Input Pulldown Resistors

### Pinout: 28-Lead PLCC (Top View)



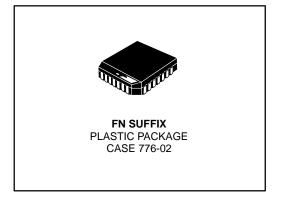
 $^{\ast}$  All VCC and VCCO pins are tied together on the die.

#### **PIN NAMES**

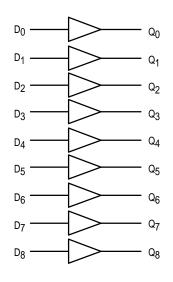
Pin	Function									
D <sub>0</sub> – D <sub>8</sub>	Data Inputs									
$Q_0 - Q_8$	Data Outputs									

# MC10E122 MC100E122

### 9-BIT BUFFER



### **LOGIC DIAGRAM**



REV 2

12/93

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### **DC CHARACTERISTICS** (VEE = VEE(min) to VEE(max); VCC = VCCO = GND)

		0°C		25°C			85°C					
Symbol	Characteristic	min	typ	max	min	typ	max	min	typ	max	Unit	Condition
lн	Input HIGH Current			200			200			200	μΑ	
IEE	Power Supply Current										mA	
	10E		41	49		41	49		41	49		
	100E		41	49		41	49		47	57		

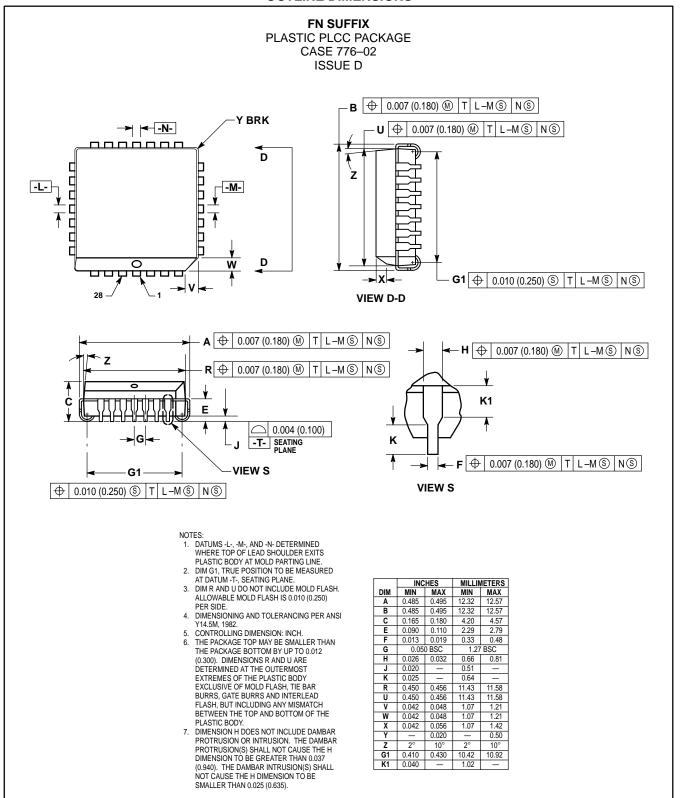
## **AC CHARACTERISTICS** ( $V_{EE} = V_{EE}(min)$ to $V_{EE}(max)$ ; $V_{CC} = V_{CCO} = GND$ )

		0°C		25°C			85°C					
Symbol	Characteristic	min	typ	max	min	typ	max	min	typ	max	Unit	Condition
<sup>t</sup> PLH <sup>t</sup> PHL	Propagation Delay to Output D to Q	150	350	500	150	350	500	150	350	500	ps	
<sup>t</sup> SKEW	Within-Device Skew D to Q		75			75			75		ps	1
t <sub>r</sub>	Rise/Fall Times 20 - 80%	300	425	800	300	425	800	300	425	800	ps	-

<sup>1.</sup> Within-device skew is defined as identical transitions on similar paths through a device.

MOTOROLA 2–2

### **OUTLINE DIMENSIONS**



### MC10E122 MC100E122

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