

Product Preview  
**Triple PECL to ECL Translator**

The MC100LVEL/EL91 is a triple PECL to ECL translator. The device receives either standard or low voltage differential PECL signals and translates them to either standard or low voltage differential ECL output signals. The LVEL device can handle the low voltage signals while the EL device is designed for the standard signals. It is possible to have low voltage signals on one side and standard signals on the other if the LVEL91 is used.

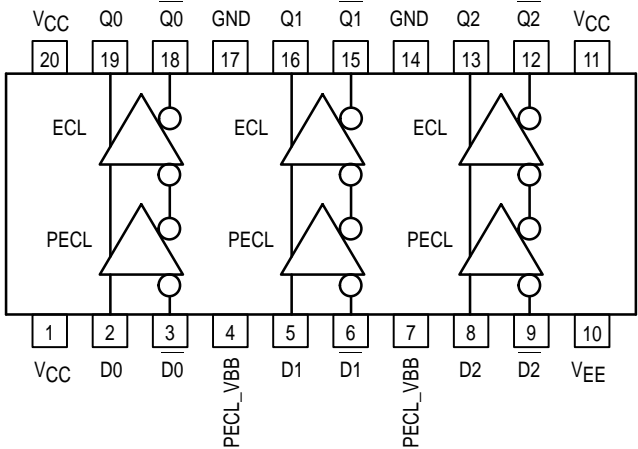
- 550ps Propagation Delays
- Fully Differential Design
- Supports both Standard and Low Voltage Operation
- 20-lead SOIC Packaging

A  $V_{BB}$  output is provided for interfacing with single ended PECL signals at the input. If a single ended input is to be used the  $V_{BB}$  output should be connected to the D input. The active signal would then drive the D input. When used the  $V_{BB}$  output should be bypassed to ground via a  $0.01\mu F$  capacitor. The  $V_{BB}$  output is designed to act as the switching reference for the EL91 under single ended input switching conditions, as a result this pin can only source/sink up to 0.5mA of current.

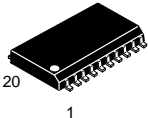
To accomplish the level translation the EL/LVEL91 requires three power rails. The  $V_{CC}$  supply should be connected to the positive supply, and the  $V_{EE}$  pin should be connected to the negative power supply. The GND pins as expected are connected to the system ground plain. Both  $V_{EE}$  and  $V_{CC}$  should be bypassed to ground via  $0.01\mu F$  capacitors.

Under open input conditions, the D input will be biased at  $V_{CC}/2$  and the D input will be pulled to GND. This condition will force the Q output to a low, ensuring stability.

Logic Diagram and Pinout: 20-Lead SOIC (Top View)



**MC100LVEL91**  
**MC100EL91**



**DW SUFFIX**  
PLASTIC SOIC PACKAGE  
CASE 751D-04

**PIN NAMES**

Pins	Function
Dn	PECL Inputs
Qn	ECL Outputs
PECL_VBB	PECL Reference Voltage Output



## LVPECL INPUT DC CHARACTERISTICS

Symbol	Characteristic	-40°C		0°C		25°C			85°C		Unit	Condition
		Min	Max	Min	Max	Min	Typ	Max	Min	Max		
V <sub>CC</sub>	Power Supply Voltage	3.0	5.25	3.0	5.25	3.0	3.3	5.25	3.0	5.25	V	
I <sub>IH</sub>	Input HIGH Current		150		150			150		150	μA	
I <sub>IL</sub>	Input LOW Current	0.5		0.5		0.5			0.5		μA	
V <sub>IH</sub>	Input HIGH Voltage <sup>1</sup>	2.135	2.420	2.135	2.420	2.135		2.420	2.135	2.420	V	V <sub>CC</sub> = 3.3V
V <sub>IL</sub>	Input LOW Voltage <sup>1</sup>	1.49	1.825	1.49	1.825	1.49		1.825	1.49	1.825	V	V <sub>CC</sub> = 3.3V
V <sub>BB</sub>	Reference Output <sup>1</sup>	1.92	2.04	1.92	2.04	1.92		2.04	1.92	2.04	V	V <sub>CC</sub> = 3.3V
I <sub>GND</sub>	Power Supply Current						6.0				mA	

1. DC levels vary 1:1 with V<sub>CC</sub>.

## PECL INPUT DC CHARACTERISTICS

Symbol	Characteristic	-40°C		0°C		25°C			85°C		Unit	Condition
		Min	Max	Min	Max	Min	Typ	Max	Min	Max		
V <sub>CC</sub>	Power Supply Voltage	4.75	5.25	4.75	5.25	4.75	5.0	5.25	4.75	5.25	V	
I <sub>IH</sub>	Input HIGH Current		150		150			150		150	μA	
I <sub>IL</sub>	Input LOW Current	0.5		0.5		0.5			0.5		μA	
V <sub>IH</sub>	Input HIGH Voltage <sup>1</sup>	3.835	4.120	3.835	4.12	3.835		4.12	3.835	4.120	V	V <sub>CC</sub> = 5.0V
V <sub>IL</sub>	Input LOW Voltage <sup>1</sup>	3.19	3.525	3.19	3.525	3.19		3.525	3.19	3.525	V	V <sub>CC</sub> = 5.0V
V <sub>BB</sub>	Reference Output <sup>1</sup>	3.62	3.74	3.62	3.74	3.62		3.74	3.62	3.75	V	V <sub>CC</sub> = 5.0V
I <sub>GND</sub>	Power Supply Current						6.0				mA	

1. DC levels vary 1:1 with V<sub>CC</sub>.

## ECL/LVECL OUTPUT DC CHARACTERISTICS

Symbol	Characteristic	-40°C		0°C		25°C			85°C		Unit	Condition
		Min	Max	Min	Max	Min	Typ	Max	Min	Max		
V <sub>EE</sub>	Power Supply Voltage EL91 LVEL91	-4.2 -3.0	-5.5 -5.5	-4.2 -3.0	-5.5 -5.5	-4.2 -3.0		-5.5 -5.5	-4.2 -3.0	-5.5 -5.5	V	
V <sub>OH</sub>	Output HIGH Voltage	-1085	-880	-1025	-880	-1025	-955	-880	-1025	-880	mV	
V <sub>OL</sub>	Output LOW Voltage	-1830	-1555	-1810	-1620	-1810	-1705	-1620	-1810	-1620	mV	
I <sub>EE</sub>	Power Supply Current						22				mA	

**MC100LVEL91****AC CHARACTERISTICS** ( $V_{EE} = -3.0V$  to  $-3.8V$ ;  $V_{CC} = 3.0V$  to  $3.8V$ )

Symbol	Characteristic	-40°C			0°C			25°C			85°C			Unit
		Min	Typ	Max	Min	Typ	Max	Min	Typ	Max	Min	Typ	Max	
t <sub>PLH</sub> t <sub>PHL</sub>	Propagation Delay Diff D to Q S.E.		550 550			550 550			550 550			550 550		ps
t <sub>SKEW</sub>	Skew Output-to-Output <sup>1</sup> Part-to-Part (Diff) <sup>1</sup> Duty Cycle (Diff) <sup>2</sup>		75 200 25			75 200 25			75 200 25			75 200 25		ps
V <sub>PP</sub>	Minimum Input Swing <sup>3</sup>	150			150			150			150			mV
V <sub>CMR</sub>	Common Mode Range <sup>4</sup> V <sub>PP</sub> < 500mV V <sub>PP</sub> ≥ 500mV	-2.0 -1.8		-0.4 -0.4	-2.1 -1.9		-0.4 -0.4	-2.1 -1.9		-0.4 -0.4	-2.1 -1.9		-0.4 -0.4	V
t <sub>r</sub> t <sub>f</sub>	Output Rise/Fall Times Q (20% – 80%)		400			400			400			400		ps

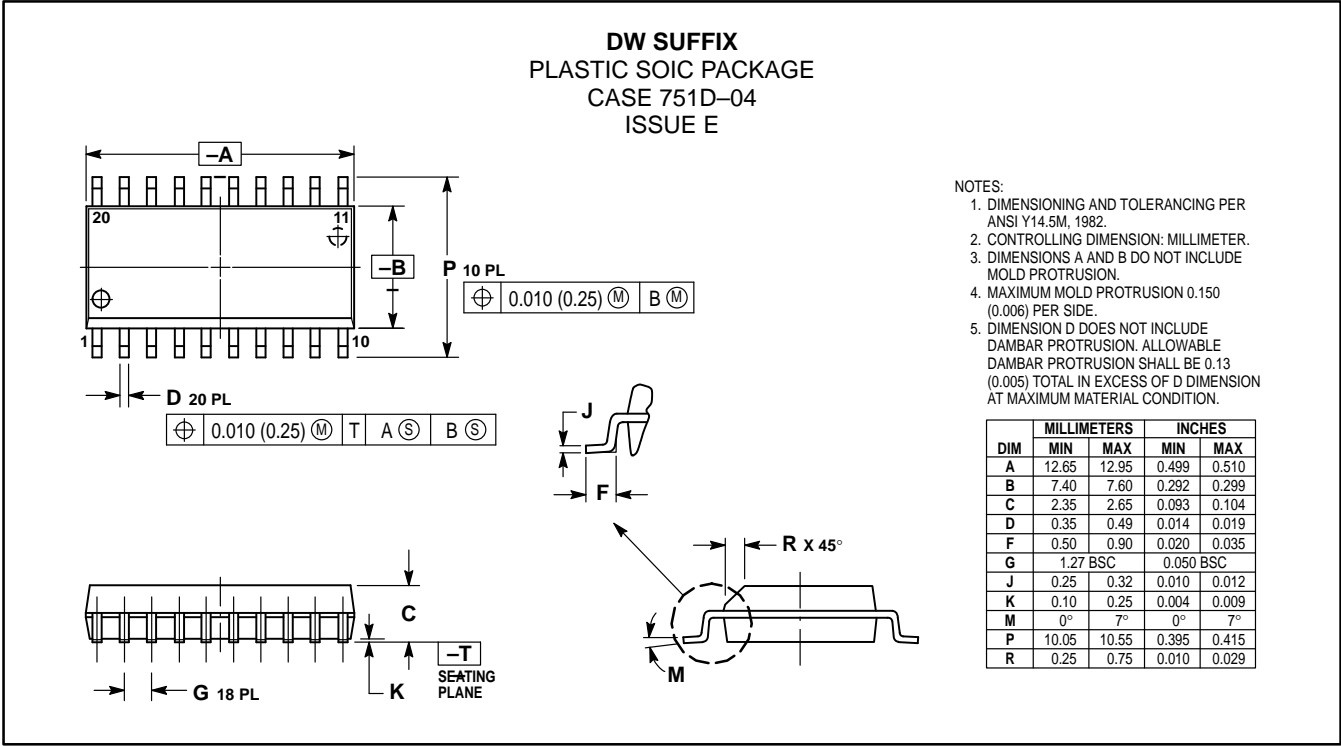
1. Skews are valid across specified voltage range, part-to-part skew is for a given temperature.
2. Duty cycle skew is the difference between a TPLH and TPHL propagation delay through a device.
3. Minimum input swing for which AC parameters guaranteed. The device has a DC gain of ≈40.
4. The CMR range is referenced to the most positive side of the differential input signal. Normal operation is obtained if the HIGH level falls within the specified range and the peak-to-peak voltage lies between V<sub>ppmin</sub> and 1V.


**MC100EL91****AC CHARACTERISTICS** ( $V_{EE} = -4.20V$  to  $-5.5V$ ;  $V_{CC} = 4.75V$  to  $5.25V$ )

Symbol	Characteristic	-40°C			0°C			25°C			85°C			Unit
		Min	Typ	Max	Min	Typ	Max	Min	Typ	Max	Min	Typ	Max	
t <sub>PLH</sub> t <sub>PHL</sub>	Propagation Delay Diff D to Q S.E.		550 550			550 550			550 550			550 550		ps
t <sub>SKEW</sub>	Skew Output-to-Output <sup>1</sup> Part-to-Part (Diff) <sup>1</sup> Duty Cycle (Diff) <sup>2</sup>		75 200 25			75 200 25			75 200 25			75 200 25		ps
V <sub>PP</sub>	Minimum Input Swing <sup>3</sup>	150			150			150			150			mV
V <sub>CMR</sub>	Common Mode Range <sup>4</sup> V <sub>PP</sub> < 500mV V <sub>PP</sub> ≥ 500mV	-3.2 -3.0		-0.4 -0.4	-3.3 -3.1		-0.4 -0.4	-3.3 -3.1		-0.4 -0.4	-3.3 -3.1		-0.4 -0.4	V
t <sub>r</sub> t <sub>f</sub>	Output Rise/Fall Times Q (20% – 80%)		400			400			400			400		ps

1. Skews are valid across specified voltage range, part-to-part skew is for a given temperature.
2. Duty cycle skew is the difference between a TPLH and TPHL propagation delay through a device.
3. Minimum input swing for which AC parameters guaranteed. The device has a DC gain of ≈40.
4. The CMR range is referenced to the most positive side of the differential input signal. Normal operation is obtained if the HIGH level falls within the specified range and the peak-to-peak voltage lies between V<sub>ppmin</sub> and 1V.

OUTLINE DIMENSIONS



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