Differential Receiver

The MC100LVEL16 is a differential receiver. The device is functionally equivalent to the EL16 device, operating from a low voltage supply. The LVEL16 exhibits a wider CMR range than its EL16 counterpart. With output transition times and propagation delays comparable to the EL16 the LVEL16 is ideally suited for interfacing with high frequency sources at 3.3V supplies.

The LVEL16 provides a V_{BB} output for either single-ended use or as a DC bias for AC coupling to the device. The V_{BB} pin should be used only as a bias for the LVEL16 as its current sink/source capability is limited. Whenever used, the V_{BB} pin should be bypassed to ground via a 0.01µf capacitor.

 $\underline{Un}der$ open input conditions, the Q input will be pulled down to V_{ee} and the Q input will be biased to $V_{CC}/2.$ This condition will force the Q output low.

- 300ps Propagation Delay
- High Bandwidth Output Transitions
- 75kΩ Internal Input Pulldown Resistors
- >2000V ESD Protection

LOGIC DIAGRAM AND PINOUT ASSIGNMENT





MC100LVEL16

PIN DESCRIPTION								
PIN	FUNCTION							
D Q V _{BB}	Data Inputs Data Outputs Ref. Voltage Output							
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MC100LVEL16

DC CHARACTERISTICS (VEE = VEE(min) to VEE(max); VCC = GND)

		–40°C			0°C			25°C			85°C			
Symbol	Characteristic	Min	Тур	Max	Unit									
IEE	Power Supply Current		17	23		17	23		17	23		18	24	mA
V _{BB}	Output Reference Voltage	-1.38		-1.26	-1.38		-1.26	-1.38		-1.26	-1.38		-1.26	V
V _{EE}	Power Supply Voltage	-3.0	-3.3	-3.8	-3.0	-3.3	-3.8	-3.0	-3.3	-3.8	-3.0	-3.3	-3.8	V
Ιн	Input HIGH Current			150			150			150			150	μΑ
١	Input LOW Current CLK CLK	0.5 600			0.5 600			0.5 600			0.5 600			μA

AC CHARACTERISTICS (V_{EE} = V_{EE}(min) to V_{EE}(max); V_{CC} = GND)

		_40°C			0°C			25°C			85°C			
Symbol	Characteristic	Min	Тур	Max	Unit									
^t PLH ^t PHL	Propagation Delay to Output (Diff) (SE)	150 100	275 275	400 450	215 165	290 290	365 415	225 175	300 300	375 425	240 190	315 315	390 440	ps
^t SKEW	Duty Cycle Skew ¹ (Diff)		5	30		5	20		5	20		5	20	ps
VPP	Minimum Input Swing ²	150			150			150			150			mV
VCMR	Common Mode Range ³ Vpp < 500mV Vpp ≥ 500mV	-2.0 -1.8		-0.4 -0.4	-2.1 -1.9		-0.4 -0.4	-2.1 -1.9		-0.4 -0.4	-2.1 -1.9		-0.4 -0.4	V
t _r t _f	Output Rise/Fall Times Q (20% – 80%)	120	220	320	120	220	320	120	220	320	120	220	320	ps

Duty cycle skew is the difference between a TPLH and TPHL propagation delay through a device.
Minimum input swing for which AC parameters guaranteed. The device has a DC gain of ~40.

Winimful minut swing for which AC parameters guaranteed. The device has a DC gain of ≈40.
The CMR range is referenced to the most positive side of the differential input signal. Normal operation is obtained if the HIGH level falls within the specified range and the peak-to-peak voltage lies between Vppmin and 1V. The lower end of the CMR range varies 1:1 with V_{EE}. The numbers in the spec table assume a nominal V_{EE} = -3.3V. Note for PECL operation, the V_{CMR}(min) will be fixed at 3.3V – |V_{CMR}(min)|.

OUTLINE DIMENSIONS



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