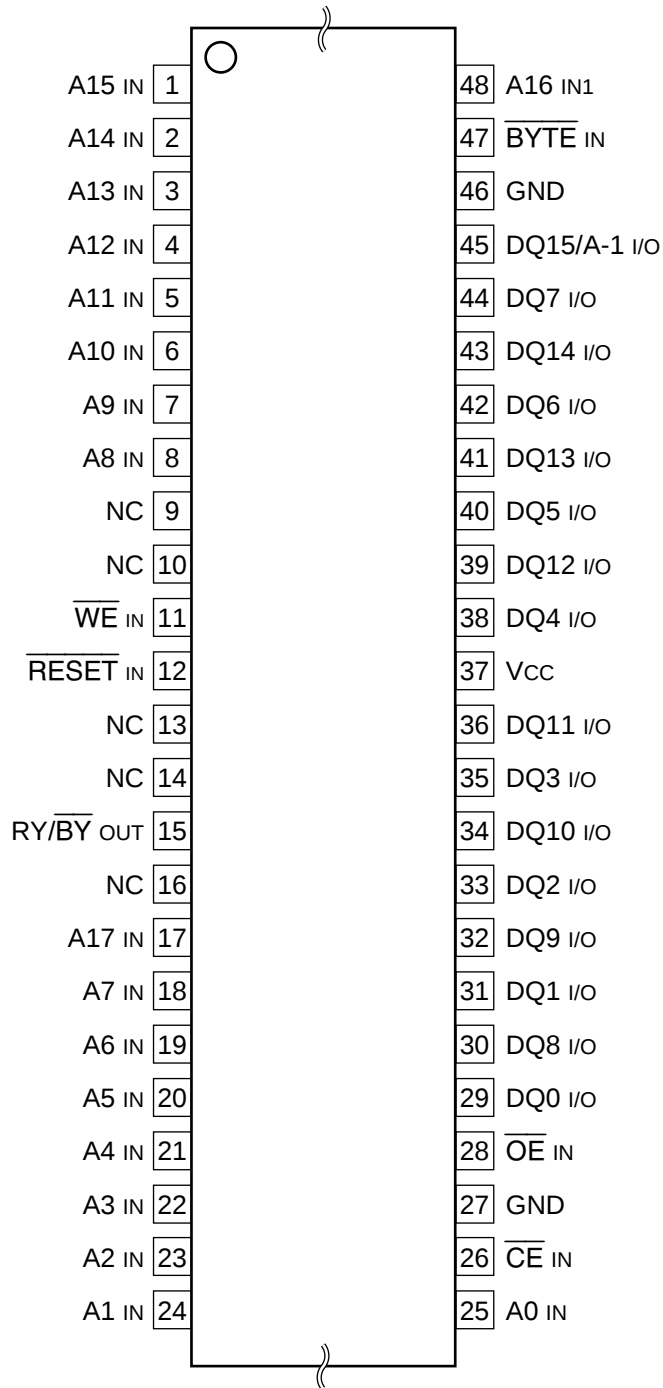


4 M (512 K × 8/256 K × 16)-BIT FLASH MEMORY

—TOP VIEW—

**INPUTS**

A0 - A17, A-1 : ADDRESS

 $\overline{BYTE}$: 8/16 BITS MODE SELECT $\overline{CE}$: CHIP ENABLE $\overline{OE}$: OUTPUT ENABLE $\overline{RESET}$: HARDWARE RESET $\overline{WE}$: WRITE ENABLE**OUTPUT**RY/ $\overline{BY}$: READY/BUSY**INPUT/OUTPUT**

DQ0 - DQ15 : DATA

OTHER

NC : NO CONNECTION

