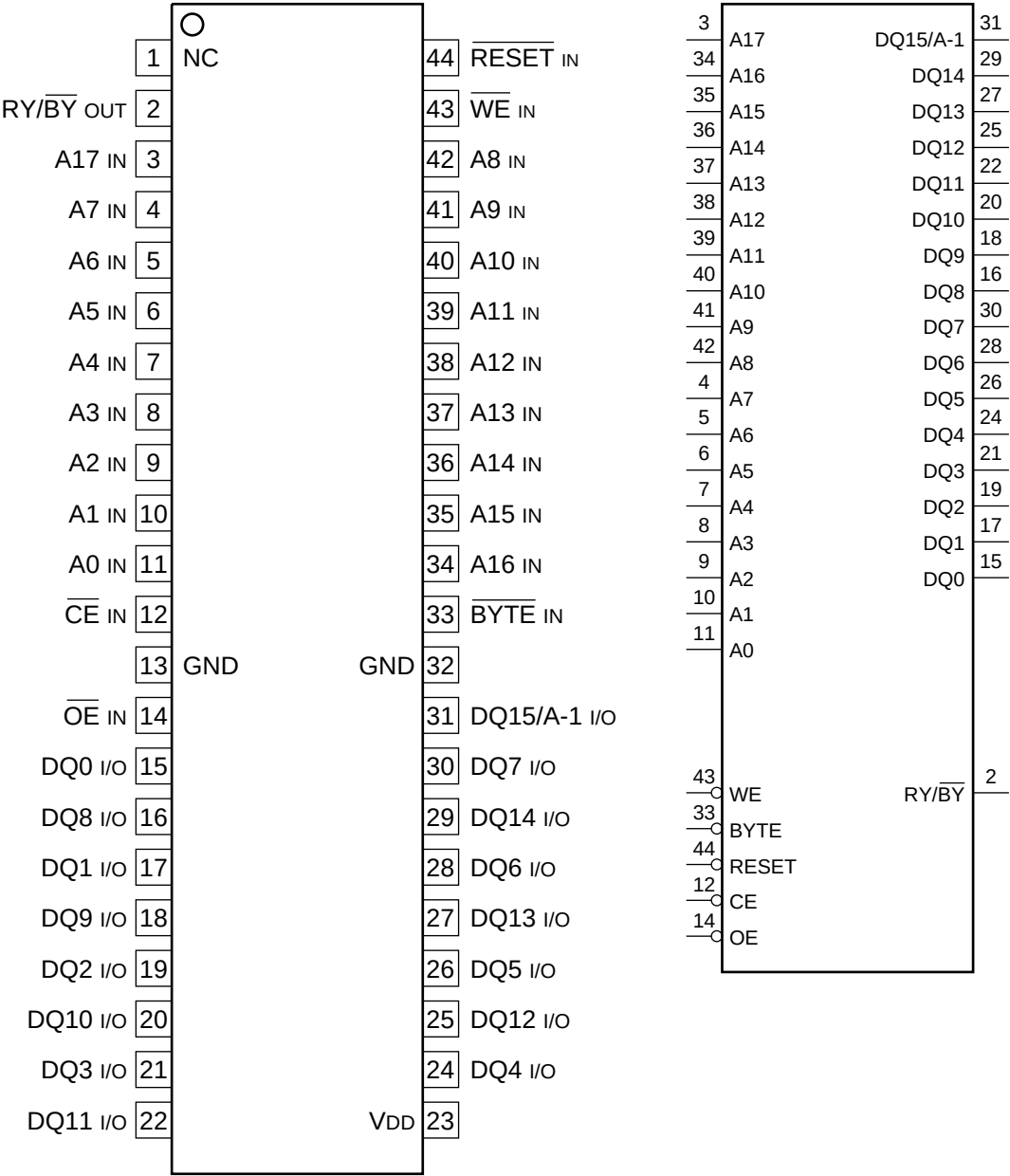


C-MOS 4 M (512 K × 8/256 K × 16)-BIT FLASH MEMORY
—TOP VIEW—



INPUT

A0 - A17, A-1 ; ADDRESS
BYTE ; 8/16 BITS MODE SELECT
CE ; CHIP ENABLE
OE ; OUTPUT ENABLE
RESET ; HARDWARE RESET
WE ; WRITE ENABLE

OUTPUT

RY/BY ; READY/BUSY

INPUT/OUTPUT

DQ0 - DQ15 ; DATA

