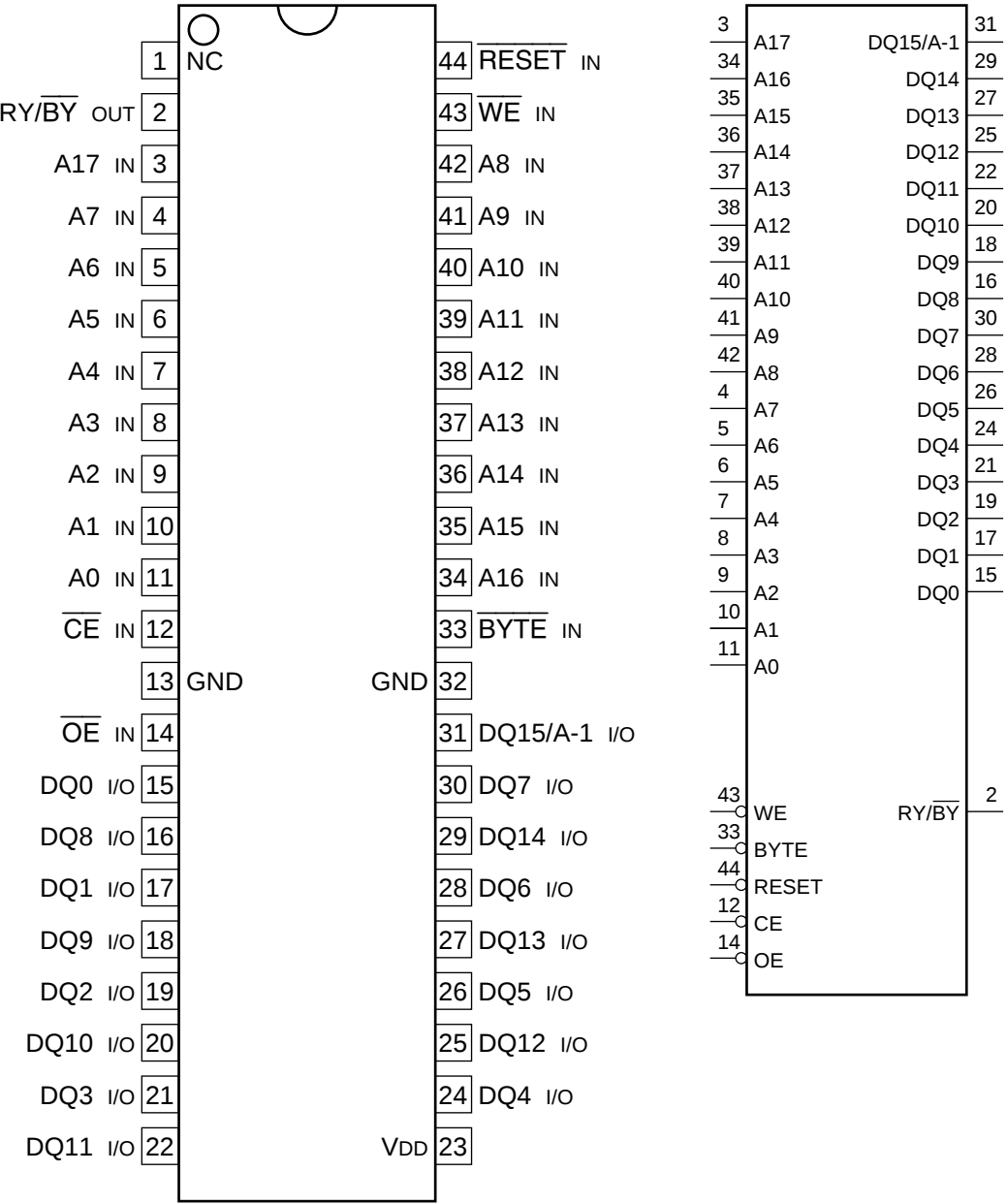


4 M (524,288 × 8/262,144 × 16)-BIT FLASH MEMORY
—TOP VIEW—



INPUTS

A0 - A17, A - 1 : ADDRESS
BYTE : 8-BIT, 16-BIT MODE SELECT
CE : CHIP ENABLE
OE : OUTPUT ENABLE
RESET : HARDWARE RESET
WE : WRITE ENABLE

OUTPUTS

RY/BY : READY/BUSY

INPUTS/OUTPUTS

DQ0 - DQ15 : DATA

