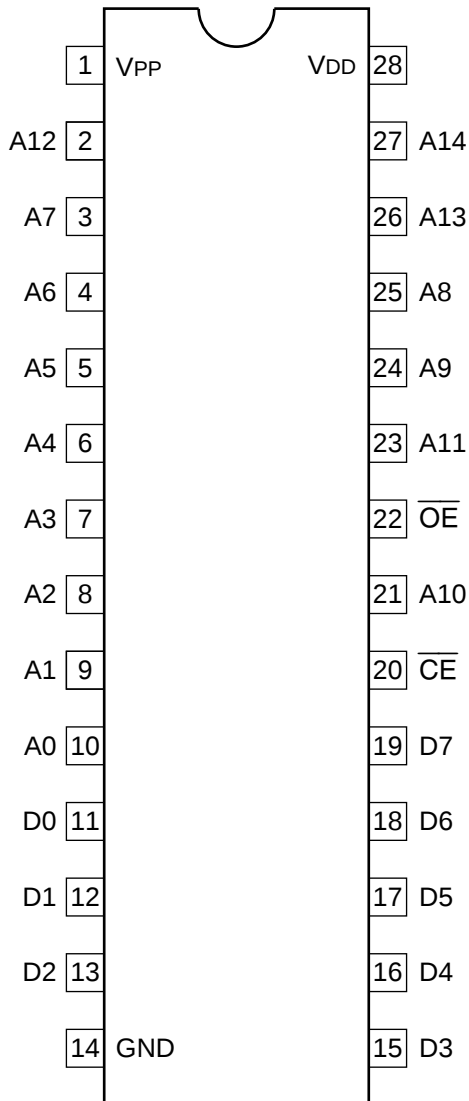


C-MOS 256 K (32 K × 8)-BIT ONE TIME PROM WITH 3-STATE OUTPUTS

—TOP VIEW—



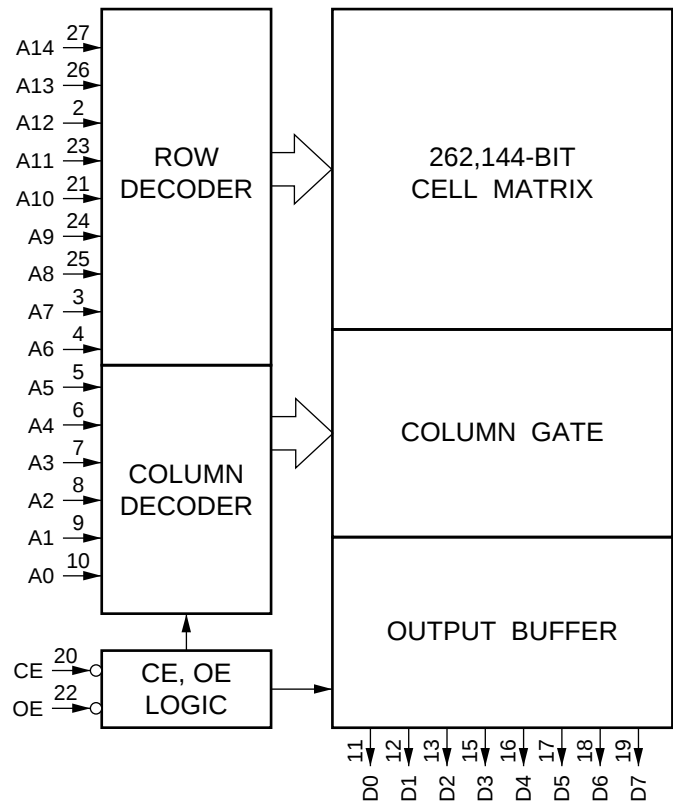
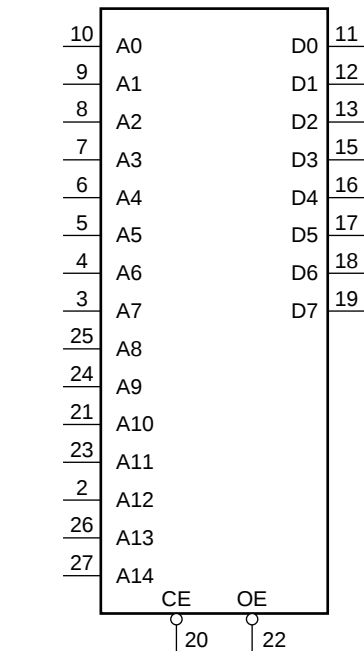
A0 - A14 : ADDRESS INPUT

 $\overline{CE}$: CHIP ENABLE

D0 - D7 : DATA OUTPUT

 $\overline{OE}$: OUTPUT ENABLE

VPP : PROGRAM POWER SUPPLY



An	$\overline{\text{CE}}$	$\overline{\text{OE}}$	V _{DD}	V _{PP}	Dn	FUNCTION
An	0	0	+5 V	+5 V	D	READ
x	0	1	+5 V	+5 V	HI-Z	OUTPUT DISABLE
An	1	x	+5 V	+5 V	HI-Z	STANDBY
An	0	1	+6 V	+12.5 V	D	PGM
An	1	0	+6 V	+12.5 V	D	PGM VERIFY
x	1	1	+6 V	+12.5 V	HI-Z	PGM INH

0 : LOW LEVEL

1 : HIGH LEVEL

x : DON'T CARE

HI-Z : HIGH IMPEDANCE