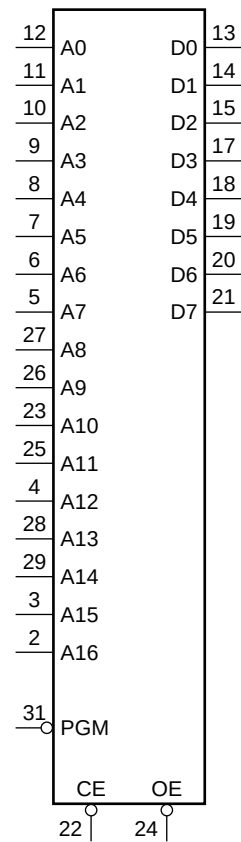
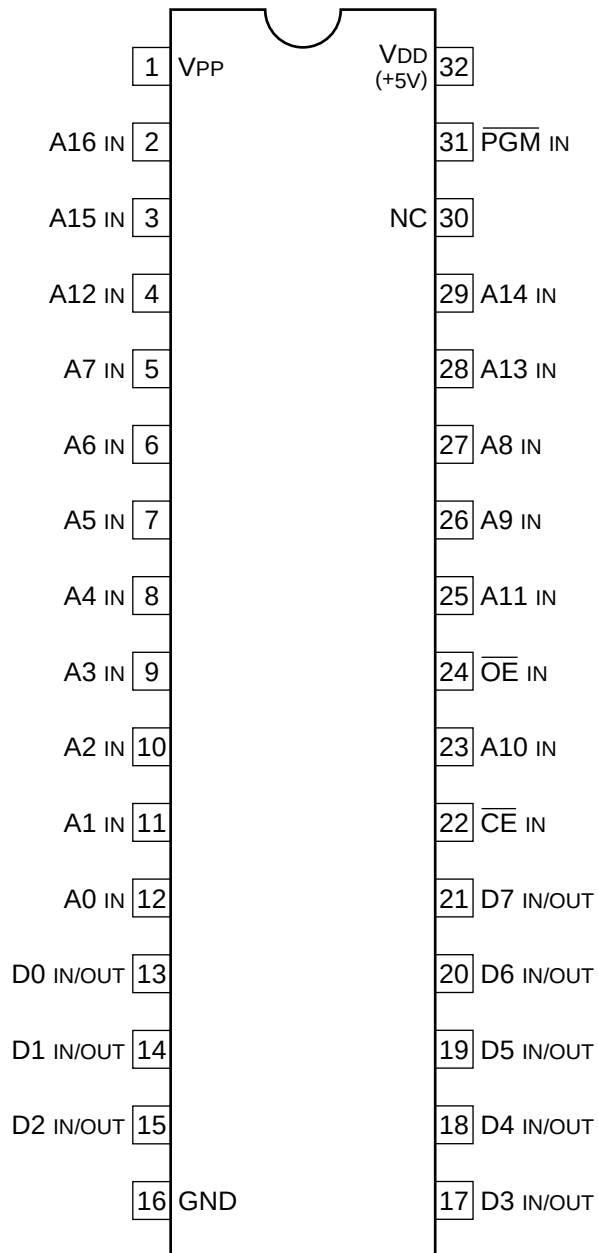

C-MOS 1M(131072×8)-BIT EPROM

-TOP VIEW-



A0-A16 ; ADDRESS INPUTS
D0-D7 ; DATA INPUTS/OUTPUTS
CE ; CHIP ENABLE INPUT
OE ; OUTPUT ENABLE INPUT
PGM ; PROGRAM INPUT/OUTPUT
ENABLE INPUT

READ MODE($V_{DD} = +5V, V_{PP} = +5V$)

OE	CE	PGM	A0-A16	D0-D7	FUNCTION
0	0	1	A IN	D OUT	ACTIVE
X	1	X	X	HI-Z	STANDBY
1	0	X	A IN	HI-Z	OUTPUT
X	0	0	A IN		DISABLE

0 ; LOW LEVEL
 1 ; HIGH LEVEL
 X ; DON'T CARE
 HI-Z ; HIGH-IMPEDANCE

PROGRAM MODE($V_{DD} = +6V, V_{PP} = +12.5V$)

MODE	OE	CE	PGM	A0-A16	D0-D7	FUNCTION
1-BYTE PROGRAM MODE	1	0	0	A IN	D IN	PROGRAM
	0	0	1	A IN	D OUT	VERIFY
	1	0	1	A IN	HI-Z	PROGRAM INHIBIT
4-BYTE PROGRAM MODE	1	1	1	A IN	D IN	PROGRAM DATA INPUT
	0	1	0	A0,A1 ; X A2-A16 ; A IN	HI-Z	PROGRAM
	0	0	1	A IN	D OUT	VERIFY
	0	1	1	A IN	HI-Z	PROGRAM INHIBIT

