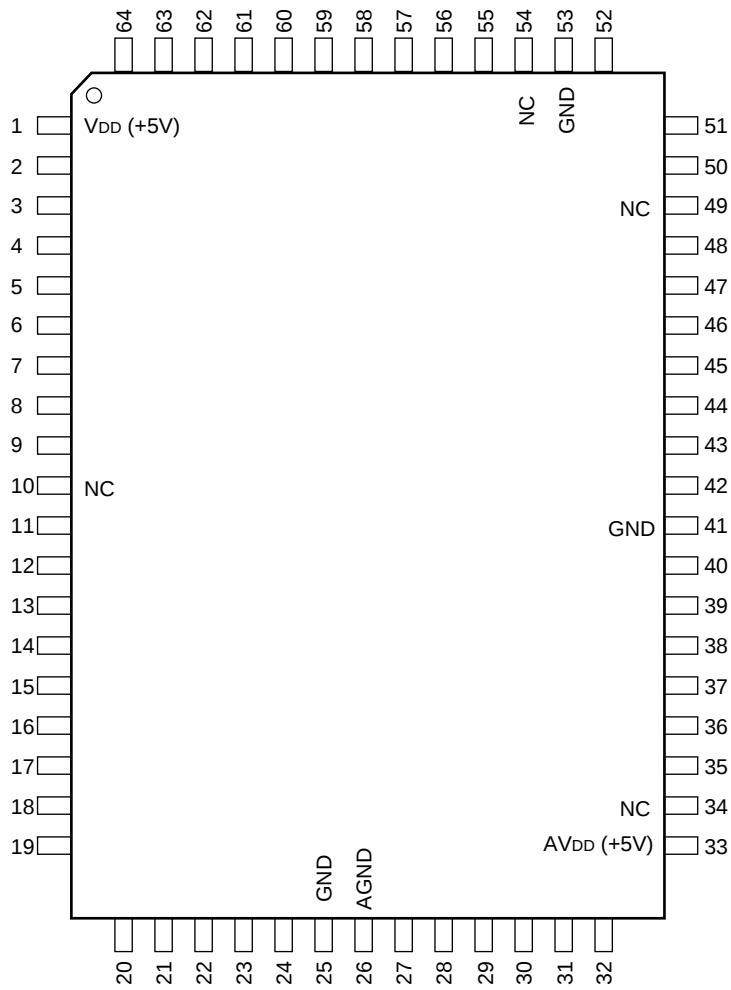

C-MOS ON SCREEN DISPLAY CONTROLLER
- TOP VIEW -



		ADR0	55	INPUT	
		ADR1	56	CIN	; CHROMA SIGNAL
		ADR2	57	CS	; CHIP SELECT
		ADR3	58	DA0 to DA7	; CHARACTER DATA
		ADR4	59	EXD	; LC CONNECTION
		ADR5	60	EXHSYN	; EXT HORIZONTAL SYNC
		ADR6	61	EXVSYN	; EXT VERTICAL SYNC
		ADR7	62	EXS	; XC CONNECTION
44	DA0	ADR8	63	RESET	; RESET
45	DA1	ADR9	64	SCLK	; SERIAL CLOCK
46	DA2	ADR10	2	SIN	; SERIAL DATA
47	DA3	ADR11	3	TEST	; TEST
48	DA4	ADR12	4	TSC	; ADDRESS BUS AND READ DISABLE
50	DA5	ADR13	5	VIN	; COMPOSITE VIDEO
51	DA6	ADR14	6	YIN	; LUMINANCE SIGNAL
52	DA7	ADR15	7		
		ADR16	8		
		ADR17	9		
		ADR18	11		
15	CS				
16	SCLK	XD	13		
17	SIN				
12	RESET	XS	20	OUTPUT	
40	TEST			ADR0 to ADR18	; ADDRESS
		VBLNK	24	B	; B DATA
14	EXD	HSYNC	22	COUT	; CHROMA SIGNAL
		VSYNC	23	G	; G DATA
21	EXS	READ	43	HSYNC	; HORIZONTAL / COMPOSITE SYNC
18	EXHSYN			R	; R DATA
19	EXVSYN	VOC	35	READ	; ROM DATA READ
		VOB	36	VBLNK	; VERTICAL BLANKING
		R	37	VOB	; CHARACTER AND BACKGROUND SIGNAL
42	TSC	G	38	VOC	; CHARACTER SIGNAL
31	V IN	B	39	VOUT	; COMPOSITE VIDEO
27	Y IN			VSYNC	; VERTICAL SYNC
29	C IN			XD	; DOT CLOCK OSC
		V OUT	32	XS	; 4 FSC OSC
		Y OUT	28	YOUT	; LUMINANCE SIGNAL
		C OUT	30		

