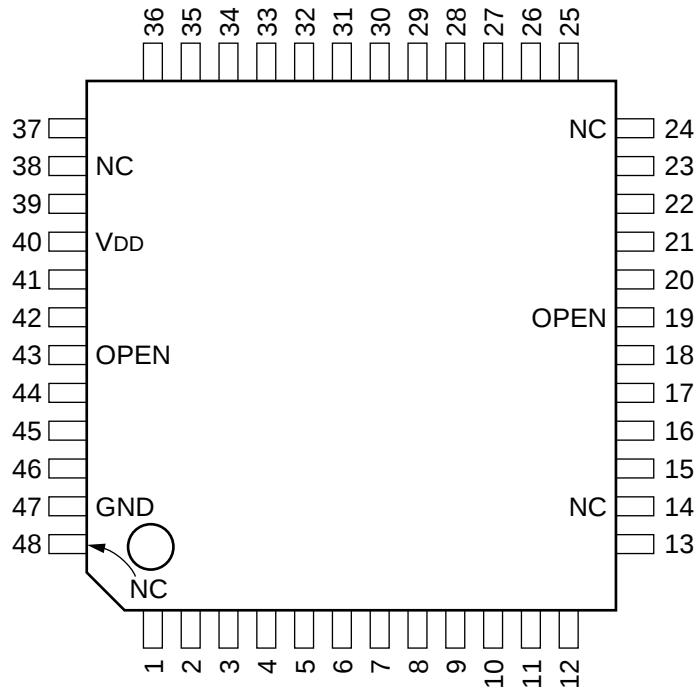


C-MOS DUAL SERIAL DATA TRANSMITTER / RECEIVER UNIT

—TOP VIEW—



NC ; NON CONNECT TERMINAL (OPEN)

OPEN ; NEVER CONNECT TERMINAL

PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL
1	I/O	DB4	17	O	TRNRDY2	33	I	CLOCK
2	I/O	DB5	18	I/O	SYNC/BRK2	34	I	RST
3	I/O	DB6	19	—	OPEN	35	I	DSR1
4	I/O	DB7	20	I	CTS2	36	O	RTS1
5	I	TRNCLK1	21	O	TRNEMP2/ST1-2	37	I	DTR1
6	I	W	22	O	TRNDT2	38	—	NC
7	I	CS1	23	O	DSR2	39	I	RCVCLK1
8	I	RSLCT0	24	—	NC	40	—	VDD
9	I	R	25	O	RTS2	41	I/O	DB0
10	O	RCVRDY1	26	O	DTR2	42	I/O	DB1
11	I	RSLCT1	27	I	RCVCLK2	43	—	OPEN
12	I	CS2	28	O	TRNRDY1	44	I/O	DB2
13	I	RCVDT2	29	I/O	SYNC/BRK1	45	I/O	DB3
14	—	NC	30	I	CTS1	46	I	RCVDT1
15	I	TRNCLK2	31	O	TRNEMP1/ST1-1	47	—	GND
16	O	RCVRDY2	32	O	TRNDT1	48	—	NC

INPUT

CLOCK	; CLOCK (4,9152 MHz) FOR REFERENCE OF TIMING
CS _n	; CHIP SELECT OF CHANNEL _n . (n = 1 OR 2)
CTS _n	; CLEAR TO SEND OF CHANNEL _n . (n = 1 OR 2)
DSR _n	; DATA SET READY OF CHANNEL _n . (n = 1 OR 2)
R	; READ ENABLE
RCVCLK _n	; RECEIVER CLOCK OF CHANNEL _n . (n = 1 OR 2)
RCVDT _n	; RECEIVE DATA OF CHANNEL _n . (n = 1 OR 2)
RSLCT _n	; REGISTERS SELECT LINE _n . (n = 0 AND 1)
RST	; RESET PULSE
TRNCLK _n	; TRANSMITTER CLOCK OF CHANNEL _n . (n = 1 OR 2)
W	; WRITE ENABLE

OUTPUT

DTR _n	; DATA TERMINAL READY OF CHANNEL _n . (n = 1 OR 2)
RCVRDY _n	; RECEIVER READY OF CHANNEL _n . (n = 1 OR 2)
RTS _n	; REQUEST TO SEND OF CHANNEL _n . (n = 1 OR 2)
TRNDT _n	; TRANSMIT DATA OF CHANNEL _n . (n = 1 OR 2)
TRNEMP _n /ST1-n	; TRANSMITTER EMPTY / BAUD RATE CLOCK OUT OF CHANNEL _n . (n = 1 OR 2)
TRNRDY _n	; TRANSMIT READY OF CHANNEL _n . (n = 1 OR 2)

INPUT/OUTPUT

DB _n	; DATA BUS LINE _n . (n = 0 TO 7)
SYNC/BRK _n	; SYNCHRONIZATION CHARACTER / BREAK CODE DETECT OF CHANNEL _n . (n = 1 OR 2)
NC	; NON CONNECT TERMINAL (OPEN)
OPEN	; NEVER CONNECT TERMINAL

