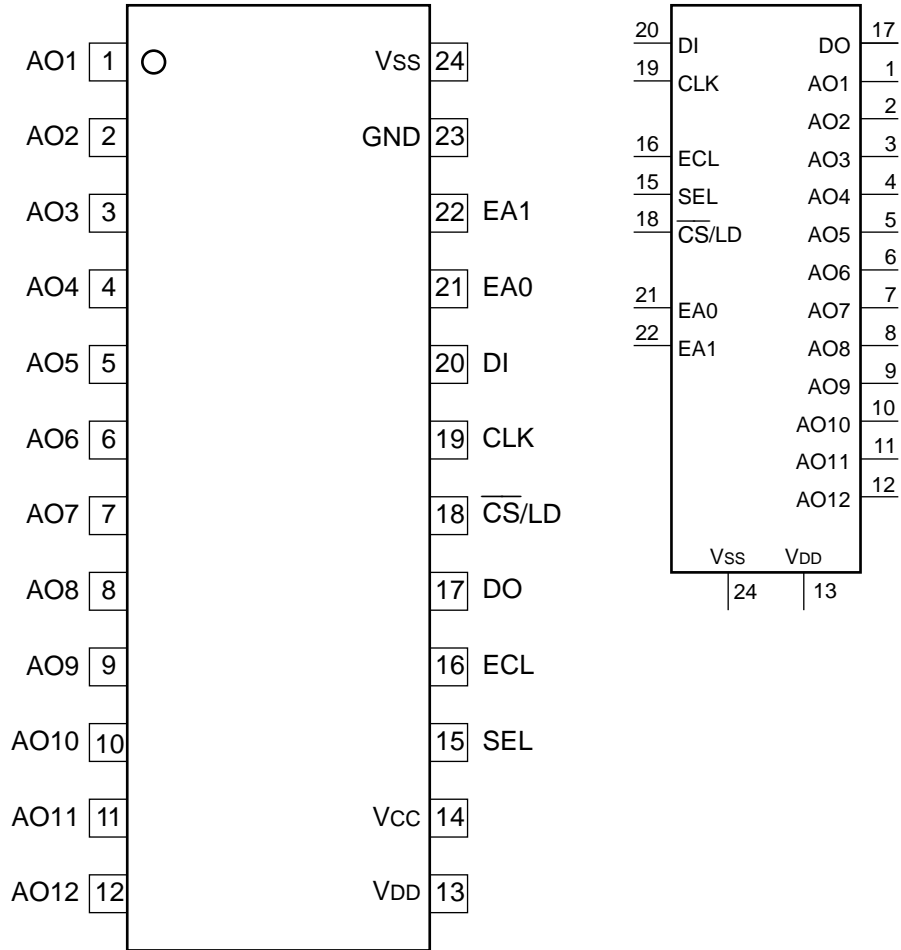


C-MOS A/D CONVERTER

—TOP VIEW—



INPUT

- CLK : SHIFT CLOCK
- $\overline{CS/LD}$: CHIP SELECT/LOAD
- DI : DATA
- EA0 - 1 : EEPROM ADDRESS
- ECL : FOR CHECK
- SEL : DATA SELECT

OUTPUT

- A01 - 12 : A/D
- DO : DATA

