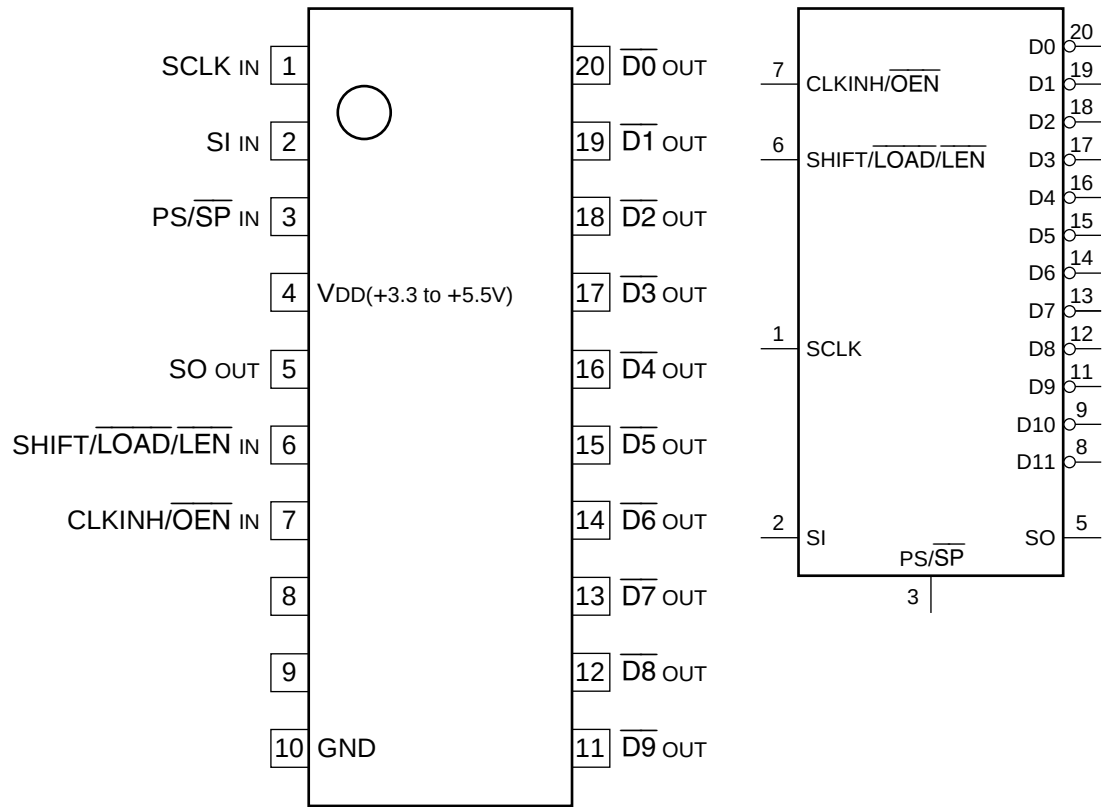

C-MOS PARALLEL-SERIAL/SERIAL-PARALLEL CONVERTER

—TOP VIEW—

**INPUT**

CLKINH/ $\overline{\text{OEN}}$; CLOCK INHIBIT/OUTPUT ENABLE
 PS/ $\overline{\text{SP}}$; PARALLEL-TO-SERIAL/SERIAL-TO-PARALLEL MODE SELECT
 SCLK ; SHIFT CLOCK
 SHIFT/ $\overline{\text{LOAD/LEN}}$; SHIFT/LOAD/LATCH ENABLE
 SI ; SERIAL DATA

OUTPUT

SO ; SERIAL DATA

INPUT/OUTPUT

$\overline{\text{D0}} - \overline{\text{D11}}$; PARALLEL DATA

PS/ $\overline{\text{SP}}$ STATUS	MODE	
	SHIFT/ $\overline{\text{LOAD/LEN}}$	CLKINH/ $\overline{\text{OEN}}$
H	SHIFT/LOAD	CLOCK INHIBIT
L	LATCH ENABLE	OUTPUT ENABLE

