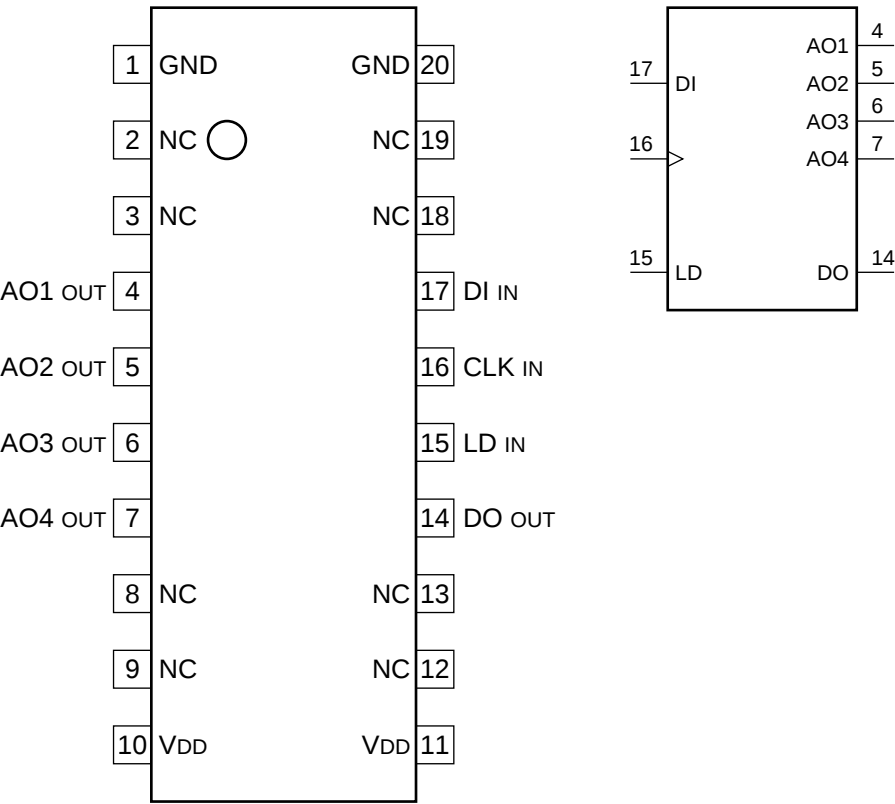


C-MOS 12-BIT D/A CONVERTER WITH OPERATIONAL AMPLIFIER

—TOP VIEW—



INPUT

- CLK : CHIFT CLOCK
- DI : SERIAL DATA
- LD : DECODER AND D/A REGISTER TO LOAD

OUTPUT

- AO1 - AO4 : ANALOG DATA
- DO : MBS BIT DATA IN 15-BIT SHIFT REGISTER

D12	D13	D14	ADDRESS SELECT
0	0	0	DON'T CARE
0	0	1	AO1 SELECT
0	1	0	AO2 SELECT
0	1	1	AO3 SELECT
1	0	0	AO4 SELECT
1	0	1	DON'T CARE
1	1	0	DON'T CARE
1	1	1	DON'T CARE

0 : LOW LEVEL

1 : HIGH LEVEL

