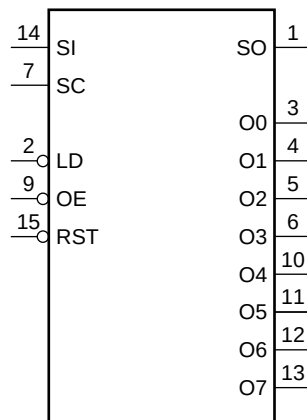
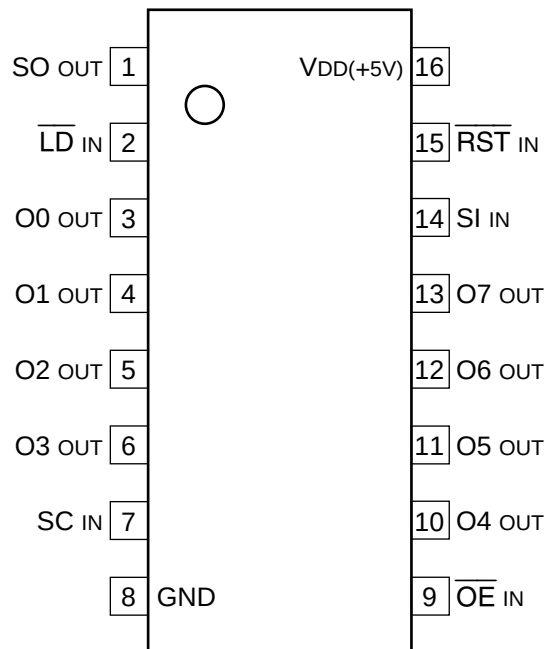

C-MOS OUTPUT EXPANDER

—TOP VIEW—



$\overline{\text{LD}}$; DATA LOAD INPUT
 O0 - O7 ; 8-BIT PARALLEL OUTPUT
 $\overline{\text{OE}}$; OUTPUT ENABLE INPUT
 $\overline{\text{RST}}$; RESET INPUT
 SC ; SHIFT CLOCK INPUT
 SI ; SERIAL DATA INPUT
 SO ; SERIAL DATA OUTPUT

