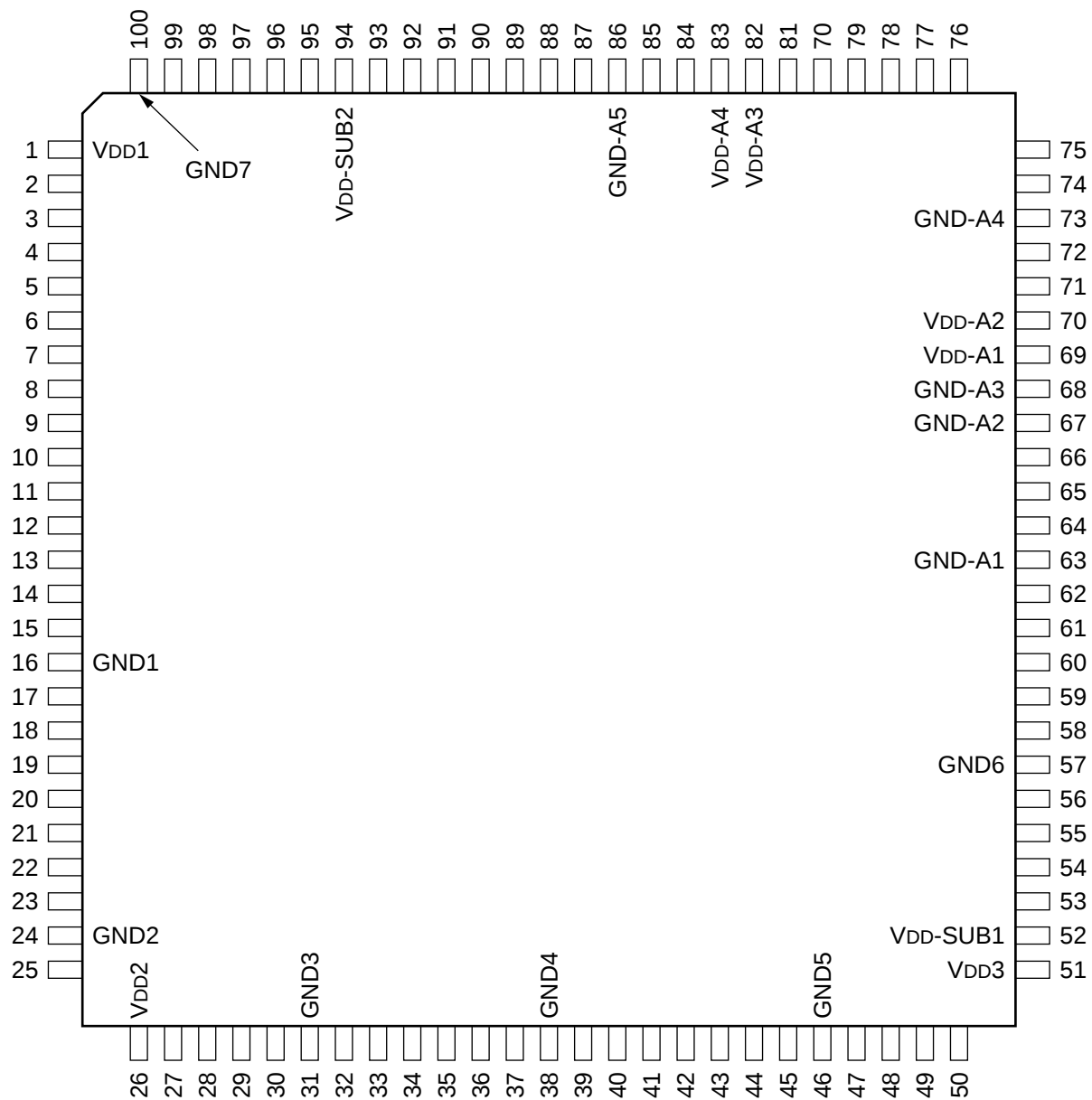


C-MOS 10BASE-T LAN CONTROLLER

—TOP VIEW—



PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL
1	—	V _{DD1}	26	—	V _{DD2}	51	—	V _{DD3}	76	I	DIP
2	O	BCS	27	I/O	SD6	52	—	V _{DD} -SUB1	77	I	DIN
3	O	BWE	28	I/O	SD13	53	I	ECS	78	O	DOP1
4	O	BOE	29	I/O	SD5	54	O	READY	79	O	DOP2
5	O	BA14	30	I/O	SD12	55	I	DMACK	80	O	DON1
6	O	BA13	31	—	GND3	56	O	DREQ	81	O	DON2
7	O	BA12	32	I/O	SD4	57	—	GND6	82	—	V _{DD} -A3
8	O	BA11	33	I/O	SD11	58	I	BHE	83	—	V _{DD} -A4
9	O	BA10	34	I/O	SD3	59	O	INT	84	I	CIP
10	O	BA9	35	I/O	SD10	60	I	EOP	85	I	CIN
11	O	BA8	36	I	RDYPOL	61	O	CLKO	86	—	GND-A5
12	O	BA7	37	I/O	SD2	62	I	CLKI	87	O	LEDT
13	I	MODE	38	—	GND4	63	—	GND-A1	88	I/O	LEDL/RLI
14	O	BA6	39	I/O	SD9	64	I	RBIAS	89	O	LEDC
15	O	BA5	40	I/O	SD1	65	O	TPOPB	90	I	RESET
16	—	GND1	41	I/O	SD8	66	O	TPOPA	91	I/O	BD7
17	O	BA4	42	I/O	SD0	67	—	GND-A2	92	I/O	BD6
18	O	BA3	43	O	SB/SW	68	—	GND-A3	93	I/O	BD5
19	O	BA2	44	I	IOW	69	—	V _{DD} -A1	94	—	V _{DD} -SUB2
20	O	BA1	45	I	IOR	70	—	V _{DD} -A2	95	I/O	BD4
21	O	BA0	46	—	GND5	71	O	TPONA	96	I/O	BD3
22	I/O	SD15	47	I	SA3	72	O	TPONB	97	I/O	BD2
23	I/O	SD7	48	I	SA2	73	—	GND-A4	98	I/O	BD1
24	—	GND2	49	I	SA1	74	O	TPIP	99	I/O	BD0
25	I/O	SD14	50	I	SA0	75	O	TPIN	100	—	GND7

INPUT

$\overline{\text{BHE}}$	: SYSTEM BUS HIGH ENABLE
CIP, CIN	: AUI COLLISION PAIR
CLKI	: CRYSTAL OSCILLATOR
DIP, DIN	: AUI RECEIVE PAIR
$\overline{\text{DMACK}}$	: DMA ACKNOWLEDGE
$\overline{\text{ECS}}$	: ENABLE CHIP SELECT
$\overline{\text{EOP}}$	: END OF PROCESS
$\overline{\text{IOR}}$	: I/O READ
$\overline{\text{IOW}}$	: I/O WRITE
MODE	: MODE SELECT
RBIAS	: BIAS RESISTOR
RDYPOL	: READY POLARITY
RESET	: CHIP RESET
SA3 - SA0	: SYSTEM ADDRESS BUS
TPIP, TPIN	: RECEIVE SIGNAL

OUTPUT

BA14 - BA0	: BUFFER MEMORY ADDRESS BUS
$\overline{\text{BCS}}$	: BUFFER RAM CHIP SELECT
$\overline{\text{BOE}}$	: BUFFER OUTPUT ENABLE
$\overline{\text{BWE}}$	: BUFFER WRITE ENABLE
CLKO	: CRYSTAL OSCILLATOR
DON1, DON2	: AUI TRANSMIT PAIR
DOP1, DOP2	: AUI TRANSMIT PAIR
DREQ	: DMA REQUEST
$\overline{\text{INT}}$	: INTERRUPT REQUEST
LEDC	: COLLISION LED
LEDT	: TRANSMIT LED
READY	: READY
$\overline{\text{SB/SW}}$	: SYSTEM BYTE/WORD
TPOPA, TPONA	: TRANSMIT SIGNAL
TPOPB, TPONB	: TRANSMIT SIGNAL

INPUT/OUTPUT

BD7 - BD0	: BUFFER MEMORY DATA BUS
LEDL/RLI	: LINK LED/RECEIVE LINK TEST
SD15 - SD0	: SYSTEM DATA BUS

