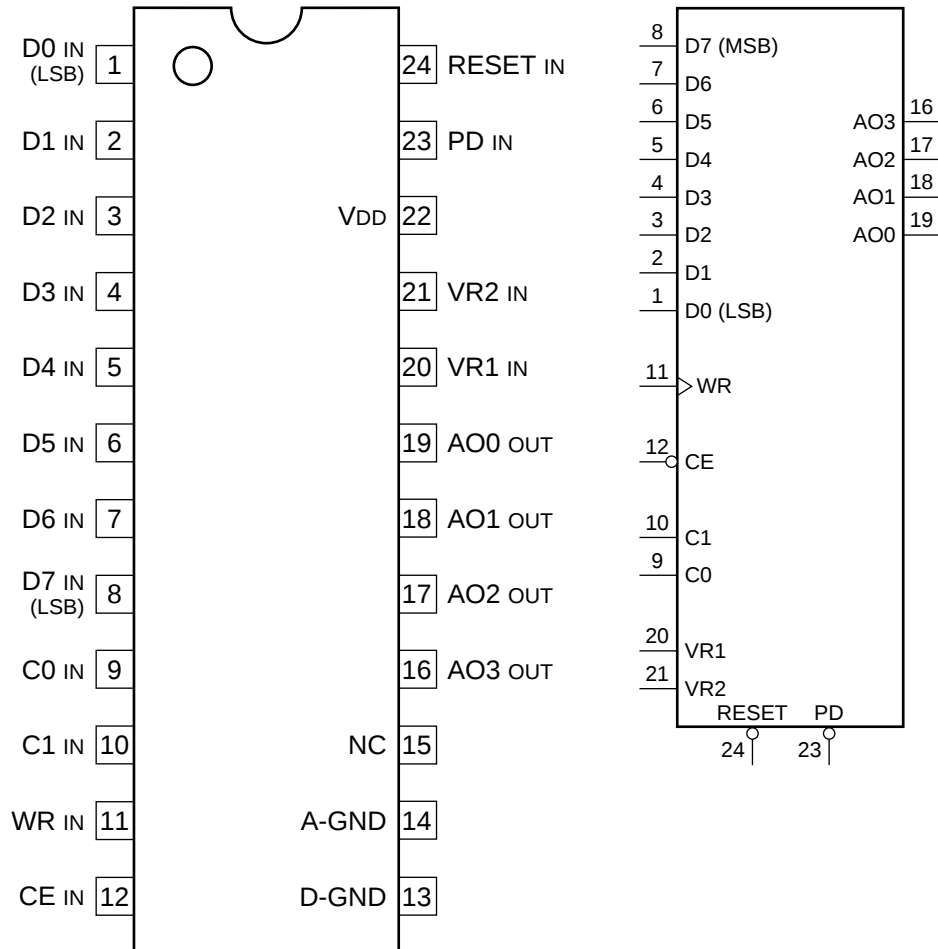


C-MOS 4-CHANNEL 8-BIT D/A CONVERTER

—TOP VIEW—

**INPUT**

C0, C1 : CHANNEL SELECT

CE : CHIP ENABLE

D0 - D7 : DATA

PD : POWER DOWN

H : NORMAL

L : POWER DOWN

(ANALOG OUT = HI-IMPEDANCE)

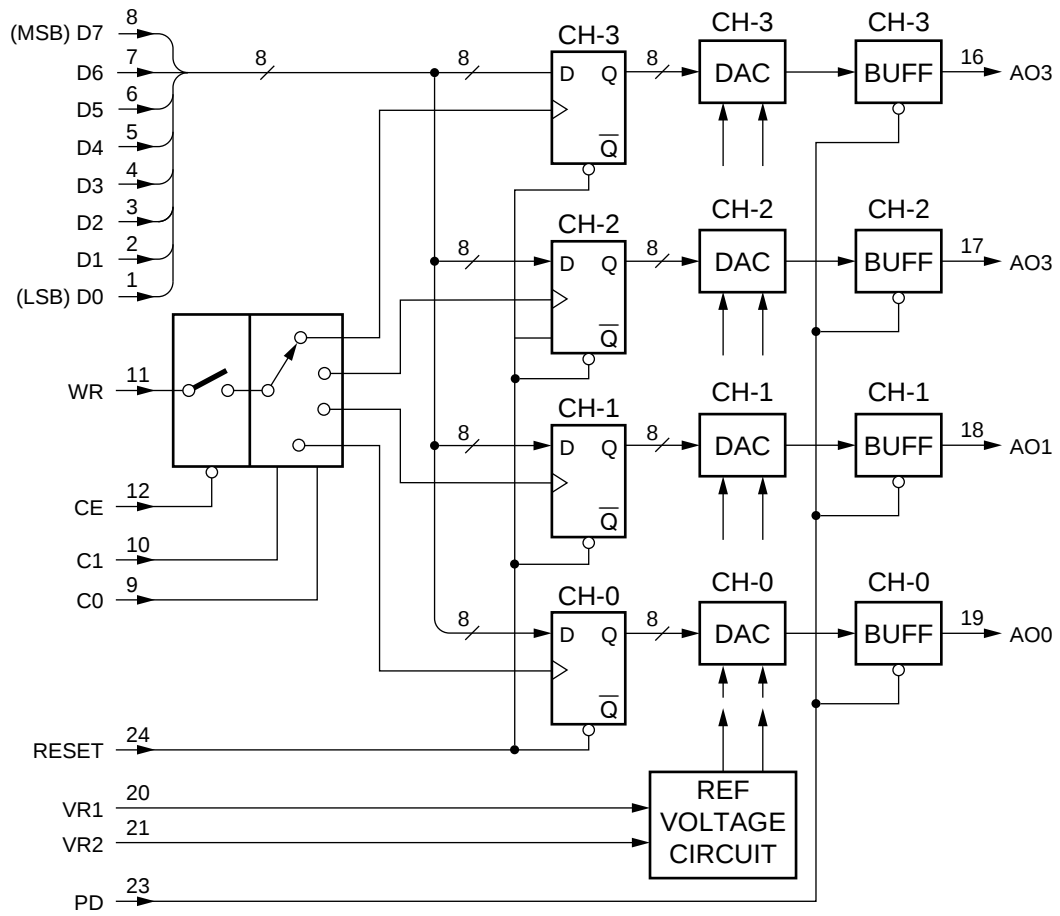
RESET : RESET

VR1, VR2 : REFERENCE VOLTAGE

WR : WRITE CLOCK

OUTPUT

AO0 - AO3 : ANALOG



FUNCTION SELECT

CONTROL INPUTS					LATCH			
RESET	CE	WR	C1	C0	CH-3	CH-2	CH-1	CH-0
1	0		0	0	HOLD	HOLD	HOLD	WRITE
1	0		0	1	HOLD	HOLD	WRITE	HOLD
1	0		1	0	HOLD	WRITE	HOLD	HOLD
1	0		1	1	WRITE	HOLD	HOLD	HOLD
1	1	x	x	x	HOLD	HOLD	HOLD	HOLD
0	x	x	x	x	RESET TO 10000000			

D/A CONVERSION

DATA INPUTS								OUTPUT VOLTAGE
D7	D6	D5	D4	D3	D2	D1	D0	VR1 = OPEN VR2 = OPEN
1	1	1	1	1	1	1	1	383/512V _{DD}
1	1	1	1	1	1	1	0	382/512V _{DD}
1	1	1	1	1	1	0	1	381/512V _{DD}
-----								-----
1	0	0	0	0	0	0	1	257/512V _{DD}
1	0	0	0	0	0	0	0	256/512V _{DD} (= 1/2V _{DD})
0	1	1	1	1	1	1	1	255/512V _{DD}
0	1	1	1	1	1	1	0	254/512V _{DD}
-----								-----
0	0	0	0	0	0	1	0	130/512V _{DD}
0	0	0	0	0	0	0	1	129/512V _{DD}
0	0	0	0	0	0	0	0	128/512V _{DD} (= 1/4V _{DD})

0 : LOW LEVEL
 1 : HIGH LEVEL
 x : DON'T CARE