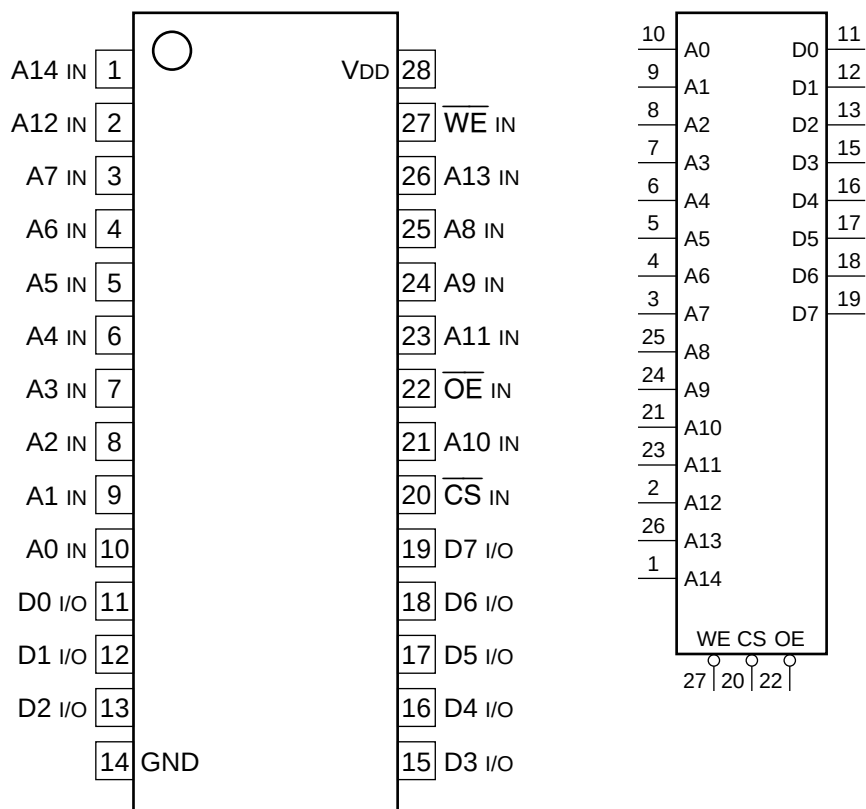


C-MOS 256 K (32,768WORD × 8)-BIT SRAM

—TOP VIEW—



A0 - A14 : ADDRESS INPUTS

D0 - D7 : DATA INPUTS/OUTPUTS

CS : CHIP SELECT INPUT

OE : OUTPUT ENABLE INPUT

WE : WRITE ENABLE INPUT

CS	OE	WE	OUTPUTS	FUNCTION
1	x	x	HI-Z	NO SELECTION
0	1	1	HI-Z	OUTPUT DISABLE
0	0	1	D OUT	READ
0	x	0	D IN	WRITE

0 : LOW LEVEL

1 : HIGH LEVEL

x : DON'T CARE

HI-Z : HIGH IMPEDANCE