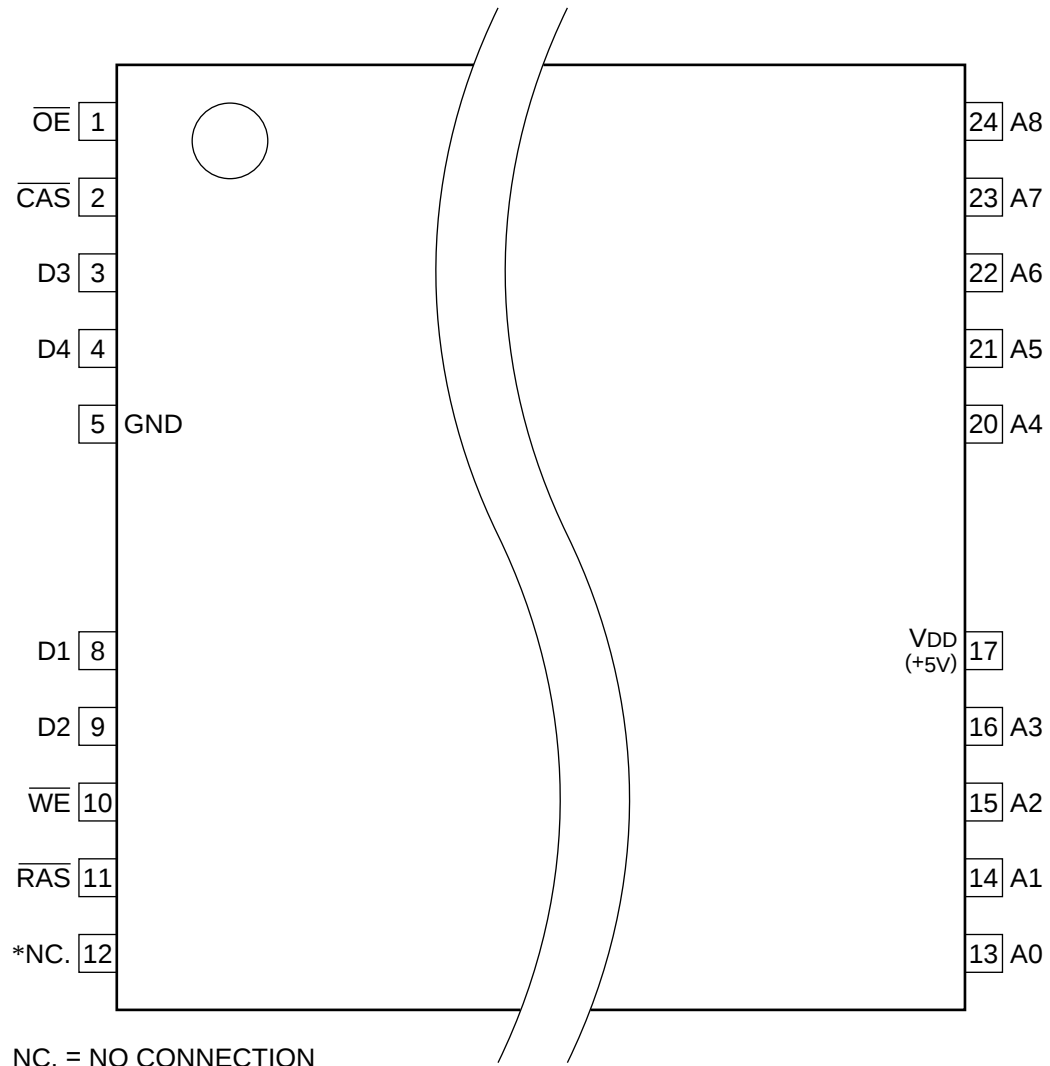
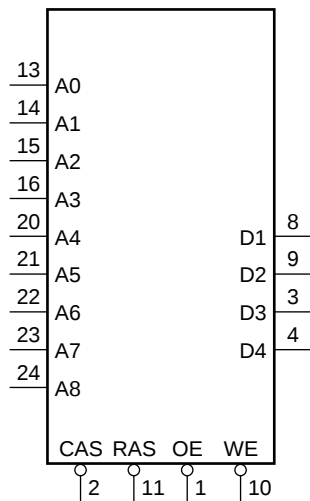

C-MOS 1M-BIT DYNAMIC RAM

-TOP VIEW-





A0-A8 ; ADDRESS INPUT
 $\overline{\text{CAS}}$; COLUMN ADDRESS STROBE
 D1-D4 ; DATA IN/OUT
 $\overline{\text{OE}}$; OUTPUT ENABLE
 $\overline{\text{RAS}}$; ROW ADDRESS STROBE
 $\overline{\text{WE}}$; WRITE ENABLE

