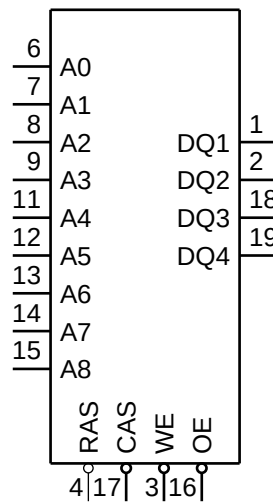
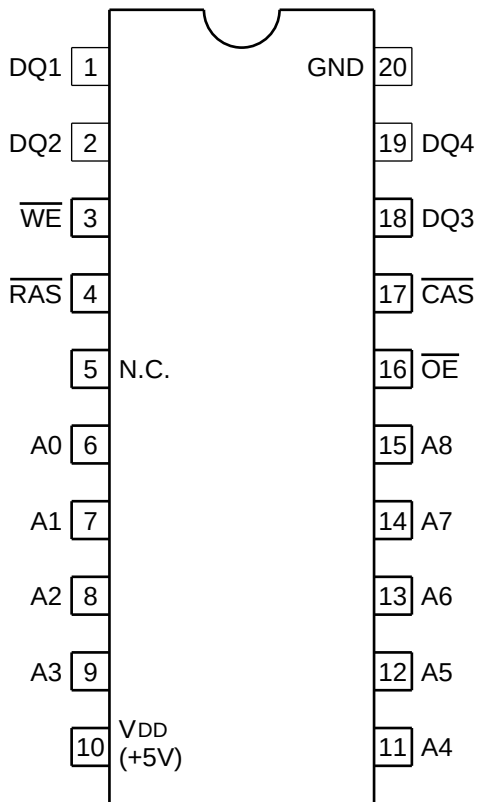

C-MOS 1M (262,144x4)-BIT DYNAMIC RAM

- TOP VIEW -



A0-8 ; ADDRESS INPUTS
 $\overline{\text{CAS}}$; COLUMN ADDRESS STROBE
 DQ1-4 ; DATA INPUTS/OUTPUTS
 $\overline{\text{OE}}$; OUTPUT ENABLE
 $\overline{\text{RAS}}$; ROW ADDRESS STROBE
 $\overline{\text{WE}}$; WRITE ENABLE

