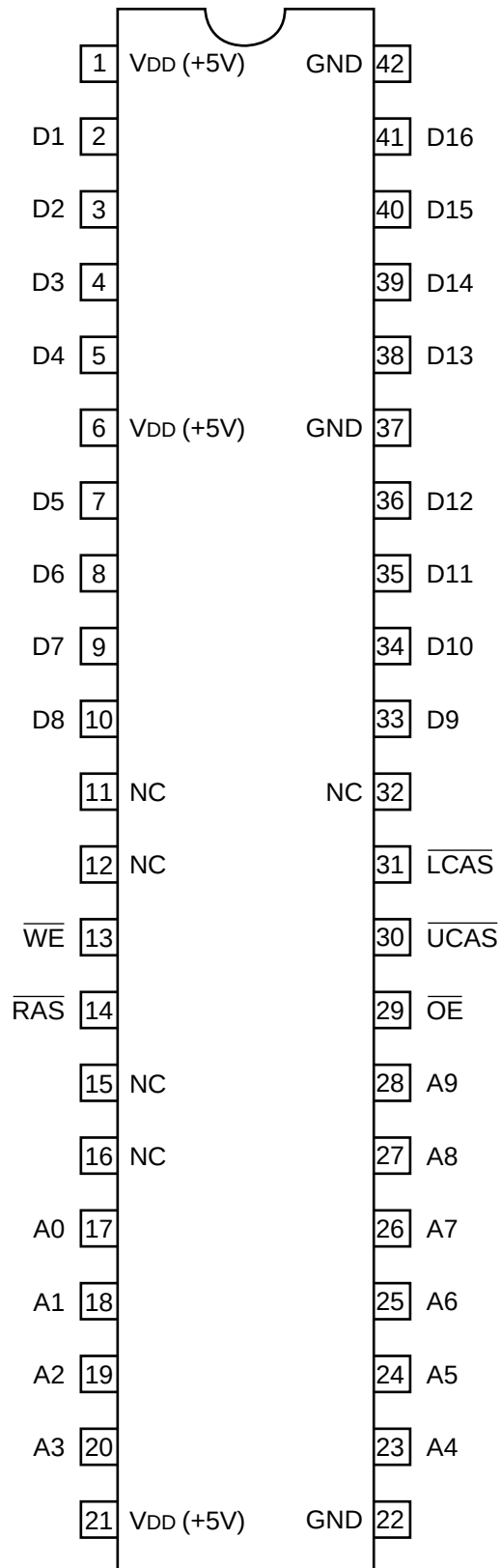


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## C-MOS 16M (1M x 16)-BIT DYNAMIC RAM

-TOP VIEW-

**INPUT**

A0 - A9 ; ADDRESS

LCAS ; LOWER COLUMN ADDRESS STROBE

UCAS ; UPPER COLUMN ADDRESS STROBE

OE ; OUTPUT ENABLE

RAS ; ROW ADDRESS STROBE

WE ; WRITE ENABLE

**I/O**

D1 - D16 ; DATA

