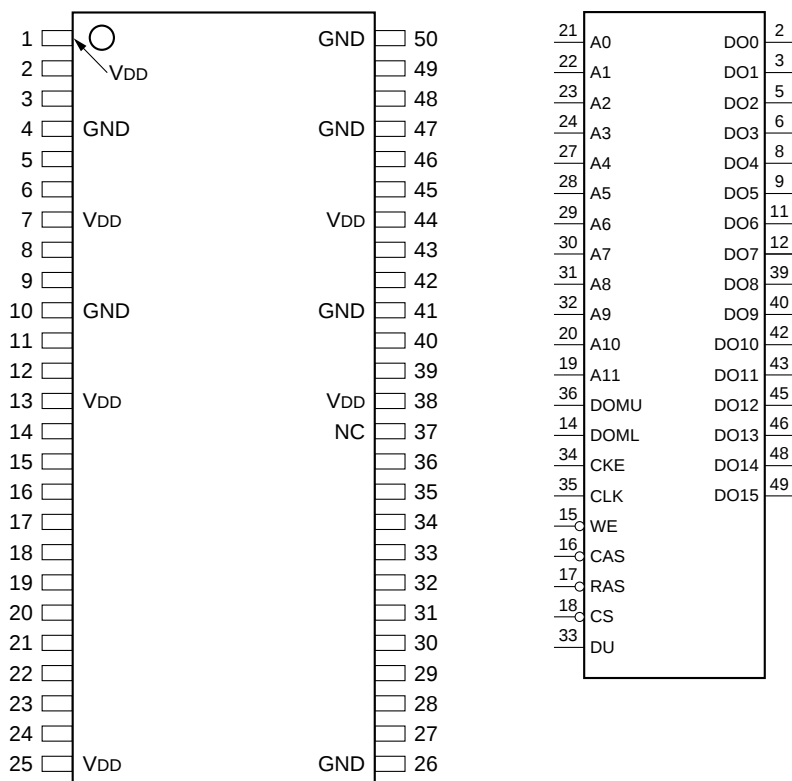


C-MOS 16 M (2,048,000 × 8)-BIT SYNCHRONOUS DRAM

—TOP VIEW—



PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL
1	—	V _{DD}	26	—	GND
2	I/O	DO0	27	I	A4
3	I/O	DO1	28	I	A5
4	—	GND	29	I	A6
5	I/O	DO2	30	I	A7
6	I/O	DO3	31	I	A8
7	—	V _{DD}	32	I	A9
8	I/O	DO4	33	—	DU
9	I/O	DO5	34	I	CKE
10	—	GND	35	I	CLK
11	I/O	DO6	36	I	DOMU
12	I/O	DO7	37	—	NC
13	—	V _{DD}	38	—	V _{DD}
14	I	DOML	39	I/O	DO8
15	I	WE	40	I/O	DO9
16	I	CAS	41	—	GND
17	I	RAS	42	I/O	DO10
18	I	CS	43	I/O	DO11
19	I	A11	44	—	V _{DD}
20	I	A10	45	I/O	DO12
21	I	A0	46	I/O	DO13
22	I	A1	47	—	GND
23	I	A2	48	I/O	DO14
24	I	A3	49	I/O	DO15
25	—	V _{DD}	50	—	GND

INPUT

A0 - A10 : ADDRESS
A11 : BANK SELECT
CAS : COLUMN ADDRESS STROBE
CKE : CLOCK ENABLE
CLK : CLOCK
CS : CHIP SELECT
DOML, DOMU : DATA OUTPUT MASKING
RAS : LOAD ADDRESS STROBE
WE : WRITE ENABLE

INPUT/OUTPUT

DO0 - DO15 : DATA

