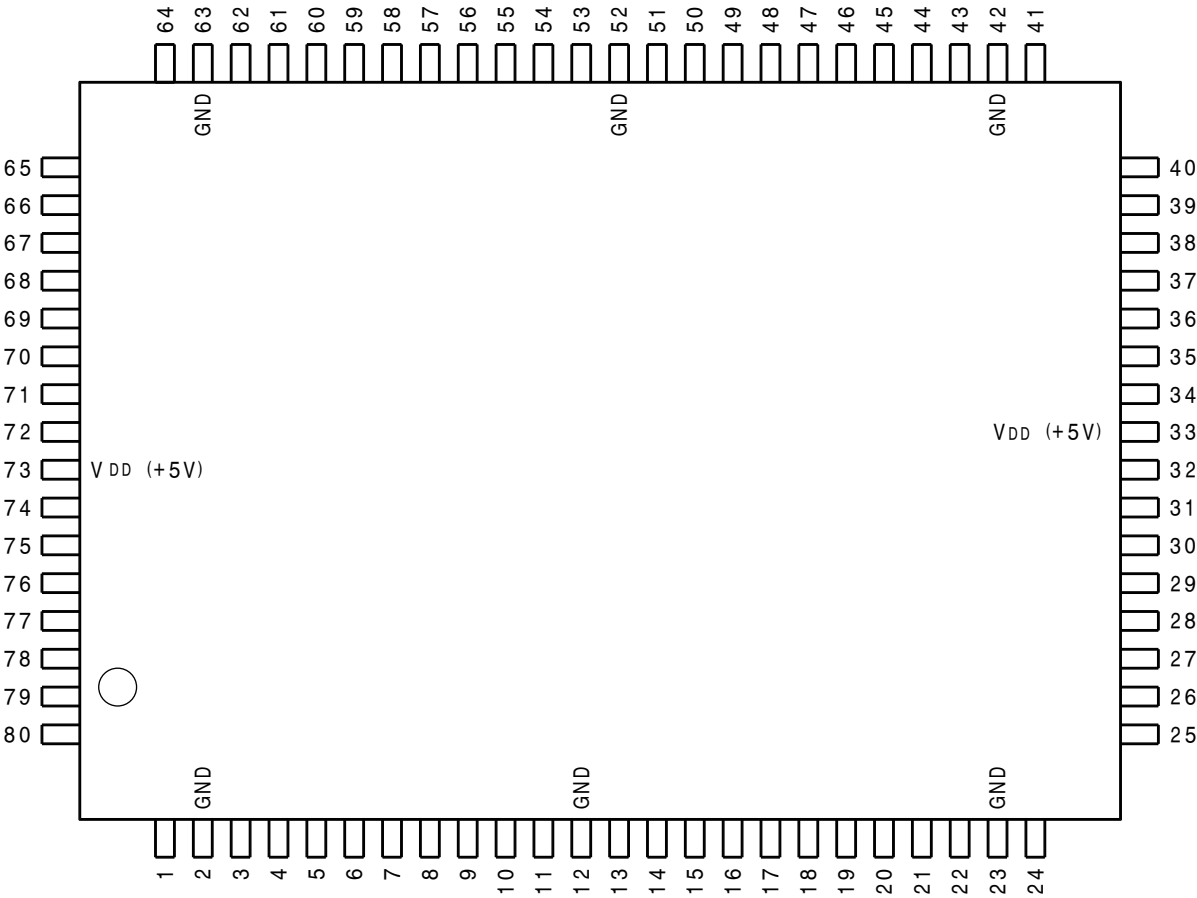

MB621873(1/2)
IL08

C-MOS DSP INTERFACE GATE ARRAY
-TOP VIEW-



PIN No.	I/O	SIGNAL	PIN No.	I/O	SIGNAL	PIN No.	I/O	SIGNAL	PIN No.	I/O	SIGNAL
1	I	MSBINV	21	I	SCK2	41	O	WCS4	61	I/O	D14
2	-	GND	22	I	CKEN2	42	-	GND	62	I/O	D15
3	I	LF/MF	23	-	GND	43	O	WCS5	63	-	GND
4	I	SLCK0	24	I	RCS2SEL	44	O	WCS6	64	I	SLOAD3
5	I	SIO	25	O	RCS2	45	O	WCS7	65	O	S03
6	I	SCK0	26	O	RCS3	46	I/O	D0	66	I	SCK3
7	I	CKEN0	27	O	RCS4	47	I/O	D1	67	I	CKEN3
8	O	LSB0	28	O	RCS5	48	I/O	D2	68	I	LSBI
9	I	LSBSEL	29	O	RCS6	49	I/O	D3	69	I	SLOAD4
10	I	ANA/DIG	30	O	RCS7	50	I/O	D4	70	O	S04A
11	I	SLCK1	31	I	RD	51	I/O	D5	71	O	S04B
12	-	GND	32	I	CS	52	-	GND	72	I	SCK4A
13	I	SI1A	33	-	VDD	53	I/O	D6	73	-	VDD
14	I	SI1B	34	I	ADR0	54	I/O	D7	74	I	SCK4B
15	I	SCK1A	35	I	ADR1	55	I/O	D8	75	I	CKEN4A
16	I	SCK1B	36	I	ADR2	56	I/O	D9	76	I	CKEN4B
17	I	CKEN1A	37	I	WE	57	I/O	D10	77	I	SLOAD5
18	I	CKEN1B	38	I	ENB	58	I/O	D11	78	O	S05
19	I	SLCK2	39	O	WCS2	59	I/O	D12	79	I	SCK5
20	I	SI2	40	O	WCS3	60	I/O	D13	80	I	CKEN5

