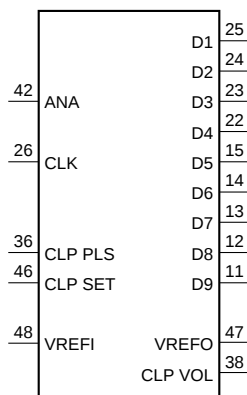
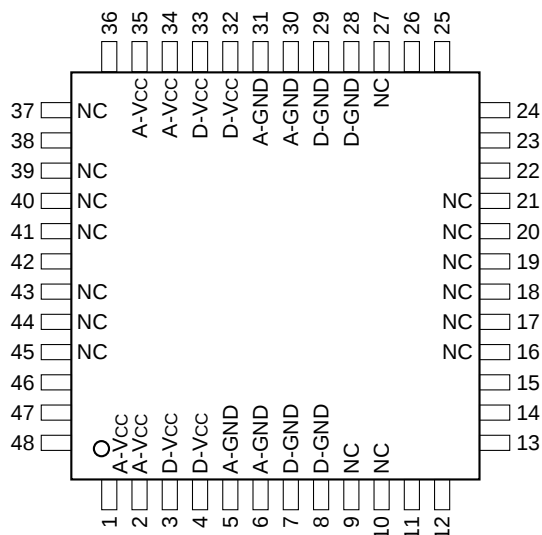


## 20 MHz 9-BIT A/D CONVERTER

—TOP VIEW—



| PIN NO. | I/O | SIGNAL   | PIN NO. | I/O | SIGNAL  |
|---------|-----|----------|---------|-----|---------|
| 1       | —   | A-Vcc    | 25      | O   | D1(MSB) |
| 2       | —   | A-Vcc    | 26      | I   | CLK     |
| 3       | —   | D-Vcc    | 27      | —   | NC      |
| 4       | —   | D-Vcc    | 28      | —   | D-GND   |
| 5       | —   | A-GND    | 29      | —   | D-GND   |
| 6       | —   | A-GND    | 30      | —   | A-GND   |
| 7       | —   | D-GND    | 31      | —   | A-GND   |
| 8       | —   | D-GND    | 32      | —   | D-Vcc   |
| 9       | —   | NC       | 33      | —   | D-Vcc   |
| 10      | —   | NC       | 34      | —   | A-Vcc   |
| 11      | O   | D9 (LSB) | 35      | —   | A-Vcc   |
| 12      | O   | D8       | 36      | I   | CLP PLS |
| 13      | O   | D7       | 37      | —   | NC      |
| 14      | O   | D6       | 38      | O   | CLP VOL |
| 15      | O   | D5       | 39      | —   | NC      |
| 16      | —   | NC       | 40      | —   | NC      |
| 17      | —   | NC       | 41      | —   | NC      |
| 18      | —   | NC       | 42      | I   | ANA     |
| 19      | —   | NC       | 43      | —   | NC      |
| 20      | —   | NC       | 44      | —   | NC      |
| 21      | —   | NC       | 45      | —   | NC      |
| 22      | O   | D4       | 46      | I   | CLP SET |
| 23      | O   | D3       | 47      | O   | VREFO   |
| 24      | O   | D2       | 48      | I   | VREFI   |

### INPUT

ANA ; ANALOG SIGNAL  
CLK ; CLOCK  
CLP PLS ; CLAMP PULSE  
CLP SET ; CLAMP VOLTAGE SETTING  
VREFI ; REFERENCE VOLTAGE

### OUTPUT

CLP VOL ; CLAMP VOLTAGE  
D1 - D9 ; DIGITAL SIGNAL (D9 : LSB, D1 : MSB)  
VREFO ; REFERENCE VOLTAGE

