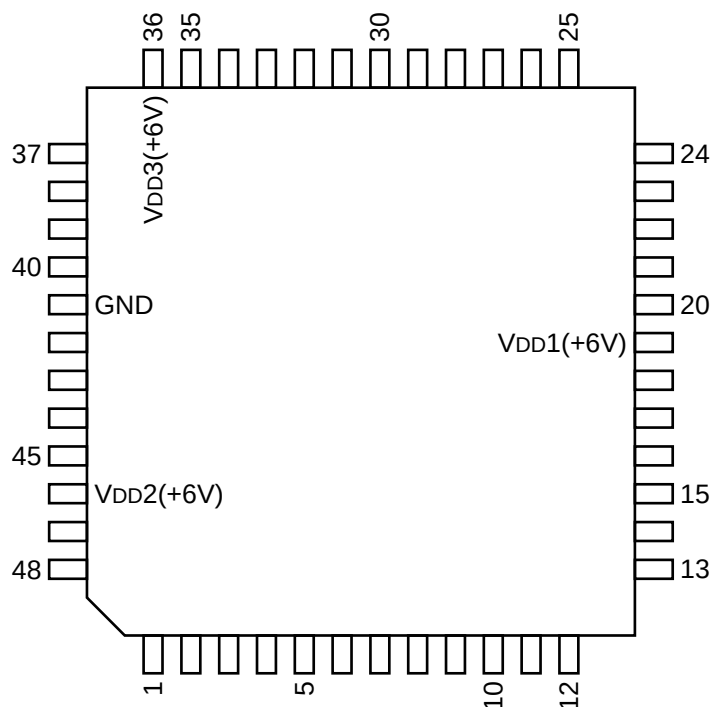

C-MOS 6-CH SWITCHING REGULATOR CONTROLLER

-TOP VIEW-



(VDD =+6V)

PIN No.	I/O	SIGNAL	PIN No.	I/O	SIGNAL	PIN No.	I/O	SIGNAL
1	O	FB A	17	—	FB(OS C)	33	O	FB F
2	I	— IN(ERROR AMP)A	18	O	V REF	34	I	VE F
3	I	IN A	19	—	VDD 1	35	O	OUT F
4	O	FB B	20	I	CTL 1	36	—	VDD 3
5	I	— IN(ERROR AMP)B	21	I	CTL 2	37	O	OUT E
6	I	+ IN(ERROR AMP)B	22	I	CTL 3	38	I	VE E
7	I	— IN(COMP)A	23	—	SCP	39	O	OUT D
8	I	IN B	24	—	CS	40	I	VE D
9	O	FB C	25	I	IN D	41	—	GND
10	I	— IN(ERROR AMP)C	26	I	— IN(ERROR AMP)D	42	I	VE C
11	I	+ IN(ERROR AMP)C	27	O	FB D	43	O	OUT C
12	I	IN C	28	I	IN E	44	I	VE B
13	—	RT	29	I	— IN(ERROR AMP)E	45	O	OUT B
14	—	CT 1	30	O	FB E	46	—	VDD 2
15	—	CT 2	31	I	IN F	47	O	OUT A
16	—	— IN(OS C)	32	I	— IN(ERROR AMP)F	48	I	VE A

INPUT

CTL1	; POWER SUPPLY CONTROL
CTL2	; Ach-Ech CONTROL
CTL3	; Fch CONTROL
-IN(E) A	; Ach ERROR AMPLIFIER INVERSION
-IN(E) B	; Bch ERROR AMPLIFIER INVERSION
-IN(E) C	; Cch ERROR AMPLIFIER INVERSION
-IN(E) D	; Dch ERROR AMPLIFIER INVERSION
-IN(E) E	; Ech ERROR AMPLIFIER INVERSION
-IN(E) F	; Fch ERROR AMPLIFIER INVERSION
-IN(C) A	; Ach COMPARATOR INVERSION
+IN(E) B	; Bch ERROR AMPLIFIER NON-INVERSION
+IN(E) C	; Cch ERROR AMPLIFIER NON-INVERSION
IN A	; Ach OUTPUT VOLTAGE FEEDBACK
IN B	; Bch OUTPUT VOLTAGE FEEDBACK
IN C	; Cch OUTPUT VOLTAGE FEEDBACK
IN D	; Dch OUTPUT VOLTAGE FEEDBACK
IN E	; Ech OUTPUT VOLTAGE FEEDBACK
IN F	; Fch OUTPUT VOLTAGE FEEDBACK
VE A	; Ach OUTPUT CURRENT SETTING
VE B	; Bch OUTPUT CURRENT SETTING
VE C	; Cch OUTPUT CURRENT SETTING
VE D	; Dch OUTPUT CURRENT SETTING
VE E	; Ech OUTPUT CURRENT SETTING
VE F	; Fch OUTPUT CURRENT SETTING

OUTPUT

FB A	; Ach ERROR AMPLIFIER
FB B	; Bch ERROR AMPLIFIER
FB C	; Cch ERROR AMPLIFIER
FB D	; Dch ERROR AMPLIFIER
FB E	; Ech ERROR AMPLIFIER
FB F	; Fch ERROR AMPLIFIER
OUT A	; Ach OUTPUT
OUT B	; Bch OUTPUT
OUT C	; Cch OUTPUT
OUT D	; Dch OUTPUT
OUT E	; Ech OUTPUT
OUT F	; Fch OUTPUT
V REF	; REFERENCE VOLTAGE

OTHER

CS	; CAPACITOR FOR SOFT START CIRCUIT
CT1	; CAPACITOR FOR DELTA WAVE FREQUENCY SETTING(A,C,Ech)
CT2	; CAPACITOR FOR DELTA WAVE FREQUENCY SETTING(B,D,Fch)
FB(OS C)	; CERAMIC OSCILLATOR
-IN(OS C)	; CERAMIC OSCILLATOR
RT	; RESISTOR FOR DELTA WAVE FREQUENCY SETTING
SCP	; CAPACITOR FOR SHORT PROTECTIVE CIRCUIT

2	-IN(E)A	FB A	1
5	-IN(E)B	FB B	4
10	-IN(E)C	FB C	9
26	-IN(E)D	FB D	27
29	-IN(E)E	FB E	30
32	-IN(E)F	FB F	33
3	IN A	OUT A	47
8	IN B	OUT B	45
12	IN C	OUT C	43
25	IN D	OUT D	39
28	IN E	OUT E	37
31	IN F	OUT F	35
48	VE A	V REF	18
44	VE B		
42	VE C		
40	VE D		
38	VE E		
34	VE F		
20	CTL 1		
21	CTL 2		
22	CTL 3		
7	-IN(C)A		
6	+IN(E)B		
11	+IN(E)C		
24	CS		
14	CT1		
15	CT2		
17	FB(OS C)		
16	-IN(OS C)		
13	RT		
23	SCP		

