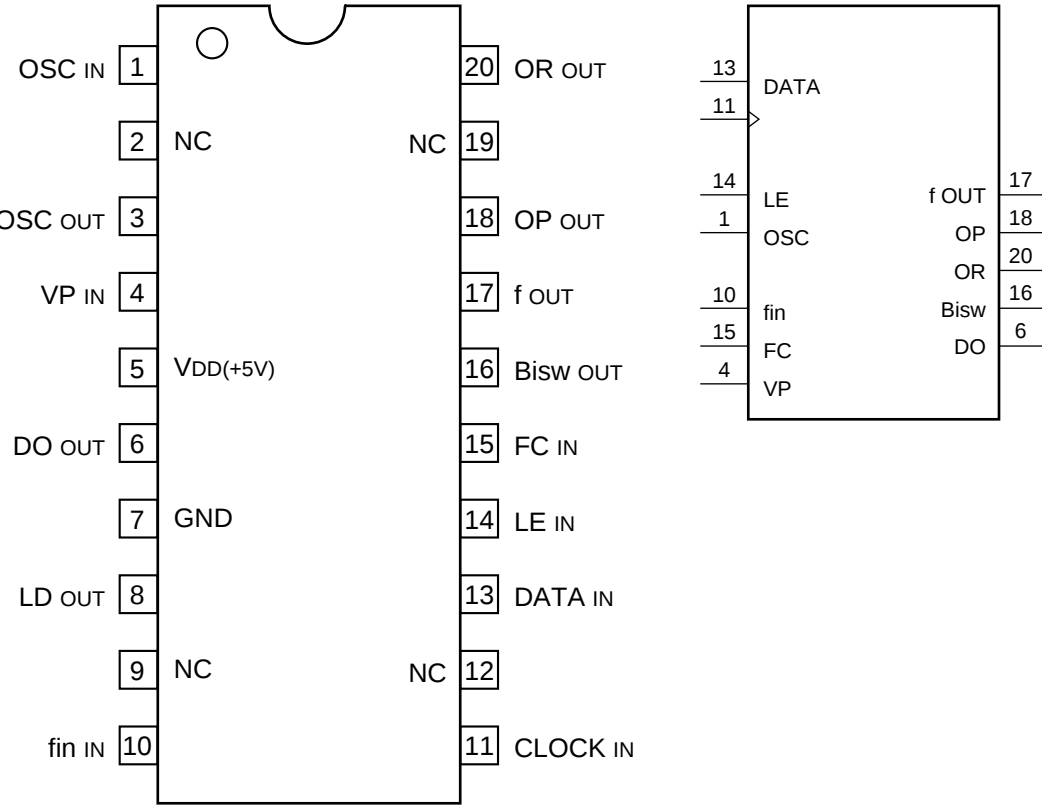


C-MOS PLL FREQUENCY SYNTHESIZER
—TOP VIEW—



INPUT

- CLOCK ; CLOCK INPUT FOR 19-BIT SHIFT REGISTER AND 16-BIT SHIFT REGISTER
- DATA ; SERIAL DATA INPUT (BINARY CODE)
- FC ; SWITCHES PHASE OF PHASE COMPARATOR
- FIN ; PRE-SCALER INPUT
- LE ; LOAD ENABLE SIGNAL INPUT
- OSCIN ; CONNECTED TO CRYSTAL OSCILLATOR (REFERENCE FREQUENCY DIVIDER INPUT)
- VP ; POWER SOURCE FOR CHARGING PUMP OUTPUT

OUTPUT

- BISW ; ANALOG SWITCH OUTPUT, NORMALLY IN HIGH IMPEDANCE
- DO ; BUILT-IN CHARGING PUMP OUTPUT
- FOUT ; MONITOR FOR PHASE COMPARATOR INPUT

FC	OUTPUT SIGNAL
H	REFERENCE FREQUENCY DIVIDER
L	COMPARISON FREQUENCY DIVDER

- LD ; PHASE COMPARATOR OUTPUT
- OSCOUT ; CONNECTED TO CRYSTAL OSCILLATOR
- Ø P AND Ø R ; OUTPUT FOR PHASE COMPARATOR EXTERNAL CHARGING PUMP

