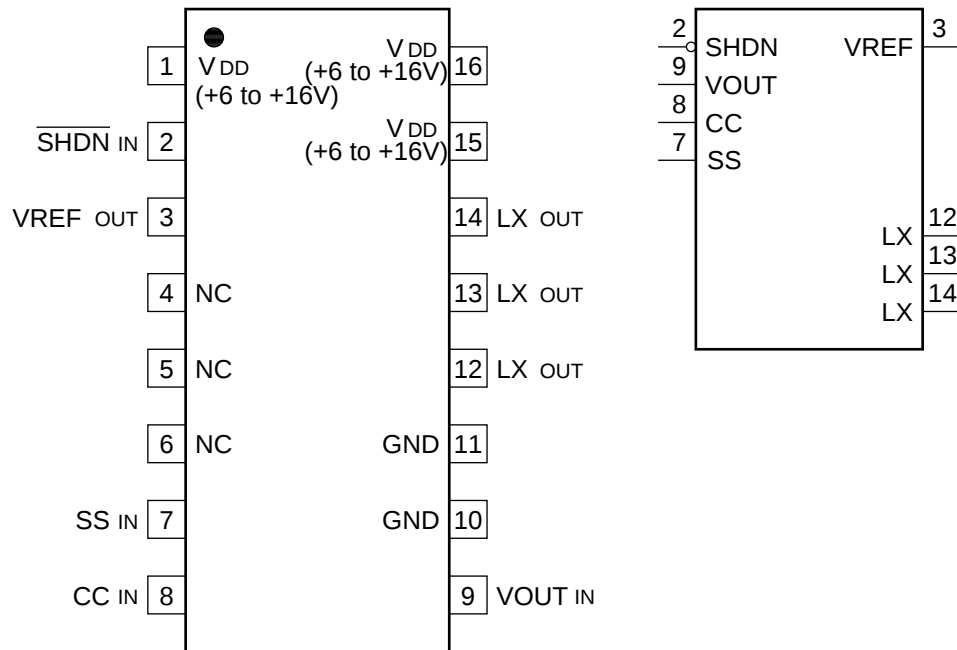

C-MOS 5V, STEP-DOWN, PWM DC-DC CONVERTER

- TOP VIEW -



CC ; COMPENSATION CAPACITOR

LX ; INTERNAL P-CHANNEL POWER-MOS FET DRAIN

$\overline{\text{SHDN}}$; SHUTDOWN (SHUTDOWN ; GND, NORMAL ; V_{DD})

SS ; SOFT START

VOUT ; OUTPUT VOLTAGE SENSE INPUT (PROVIDES REGULATION FEEDBACK SENSING)
(CONNECT TO +5V OUTPUT)

VREF ; REFERENCE VOLTAGE OUTPUT (+1.23V)

