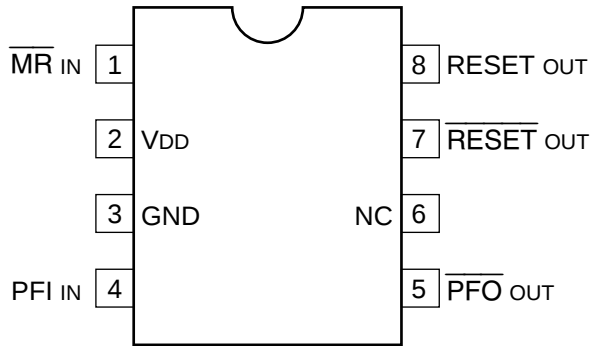


C-MOS MICROPROCESSOR SUPERVISORY CIRCUIT

—TOP VIEW—



INPUT

$\overline{\text{MR}}$: MANUAL RESET
 PFI : POWER FAIL

OUTPUT

$\overline{\text{PFO}}$: POWER FAIL

