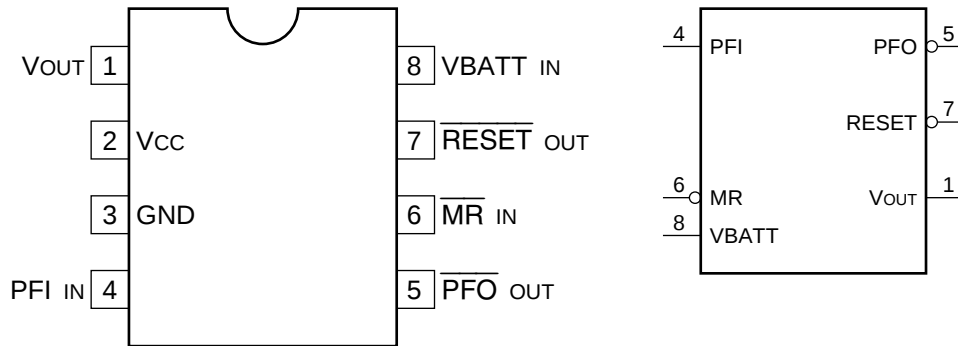


MICROPROCESSOR SUPERVISORY CIRCUIT

—TOP VIEW—

**INPUT** $\overline{\text{MR}}$: MANUAL RESET

PFI : POWER FAIL

VBATT : BACKUP BATTERY

OUTPUT $\overline{\text{PFO}}$: POWER FAIL $\overline{\text{RESET}}$: RESET

VOUT : POWER SUPPLY TO CMOS RAM

