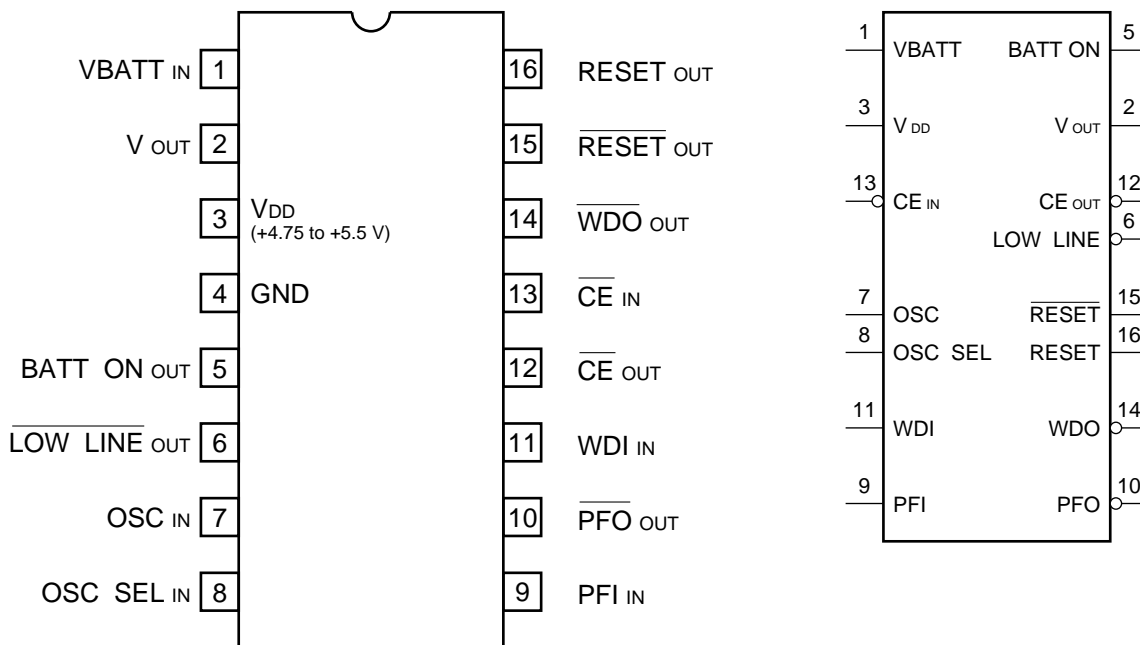

C-MOS MICROPROCESSOR SUPERVISORY CIRCUIT

- TOP VIEW -



INPUT

- $\overline{\text{CE}}$; CHIP ENABLE
- OSC ; OSCILLATOR
- OSC SEL ; OSCILLATOR SELECT
- PFI ; POWER FAIL
- VBATT ; BACKUP BATTERY
- WDI ; WATCHDOG

OUTPUT

- BATT ON ; BATTERY ON
- $\overline{\text{CE}}$; CHIP ENABLE
- $\overline{\text{LOW LINE}}$; V_{DD} : UNDER RESET-THRESHOLD = LOW
V_{DD} : OVER RESET-THRESHOLD = HIGH
- PFO ; POWER FAIL
- RESET ; NORMAL = ACTIVE HIGH
OPEN DRAIN = RESET INVERT
- $\overline{\text{RESET}}$; V_{DD} : UNDER RESET-THRESHOLD = LOW
- V ; SUPPLY VOLTAGE
- $\overline{\text{WDO}}$; WATCHDOG

