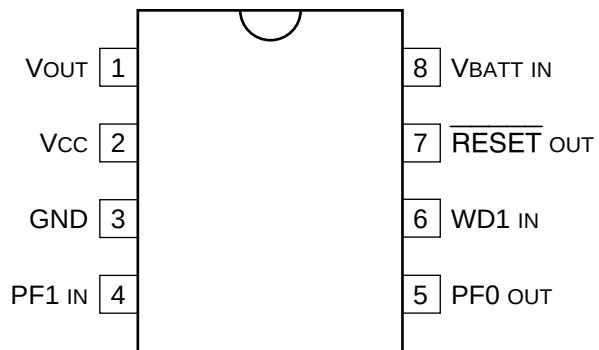


MICROPROCESSOR SUPERVISORY CIRCUITS

—TOP VIEW—

**INPUTS**

PFI : POWER FAIL COMPARATOR
 VBATT : BACK UP BATTERY
 WD1 : WATCHDOG

OUTPUTS

PF0 : POWER FAIL COMPARATOR
 RESET : RESET PULSE
 VOUT : VCC OR VBATT

