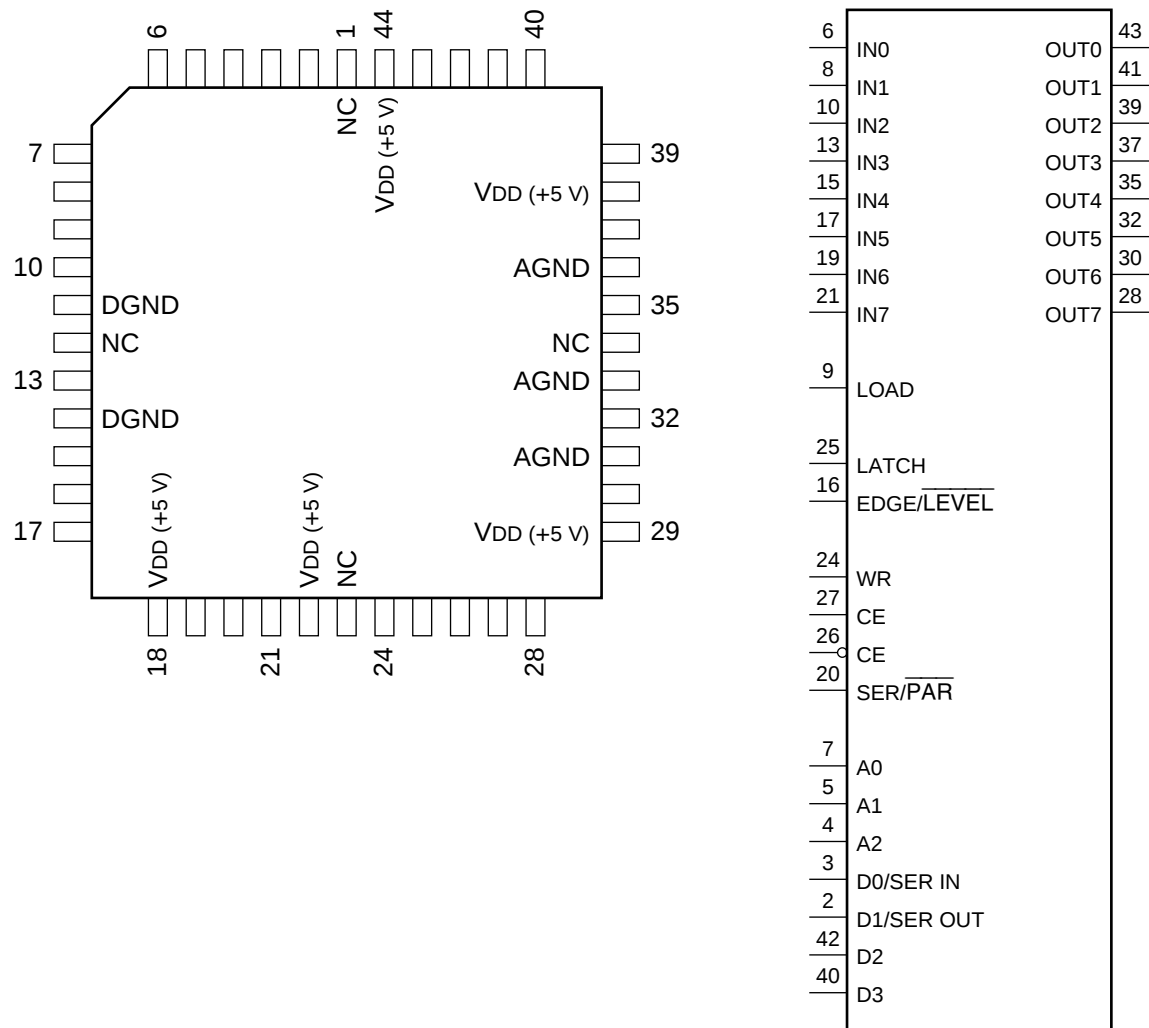


—TOP VIEW—



SERIAL/ PARALLEL	D3	D2	D1	D0	A2 - A0	COMMENT
H	X	X	SERIAL OUTPUT	SERIAL INPUT	X	32-BIT SERIAL MODE
L	H	PARALLEL INPUT	PARALLEL INPUT	PARALLEL INPUT	OUTPUT BUFFER ADDRESS	PARALLEL MODE D0•D2 = CONTROL CODE
L	L	PARALLEL INPUT	PARALLEL INPUT	PARALLEL INPUT	OUTPUT BUFFER ADDRESS	PARALLEL MODE D0•D2 = INPUT ADDRESS

NOTE : X = DON'T CARE H = +5 V, L = 0 V

(V_{DD} = +5 V, V_{SS} = -5 V)

PIN No.	I/O	SIGNAL	PIN No.	I/O	SIGNAL
1	—	NC	23	—	NC
2	I	D1/SER OUT	24	I	WR
3	I	D0/SER IN	25	I	LATCH
4	I	A2	26	I	$\overline{\text{CE}}$
5	I	A1	27	I	CE
6	I	IN0	28	O	OUT7
7	I	A0	29	—	V _{DD}
8	I	IN1	30	O	OUT6
9	I	LOAD	31	—	AGND
10	I	IN2	32	O	OUT5
11	—	DGND	33	—	AGND
12	—	NC	34	—	NC
13	I	IN3	35	O	OUT4
14	—	DGND	36	—	AGND
15	I	IN4	37	O	OUT3
16	I	EDGE/LEVEL	38	—	V _{SS}
17	I	IN5	39	O	OUT2
18	—	V _{DD}	40	I	D3
19	I	IN6	41	O	OUT1
20	I	SER/ $\overline{\text{PAR}}$	42	I	D2
21	I	IN7	43	O	OUT0
22	—	V _{SS}	44	—	V _{DD}

INPUT

A0 - 2 : OUTPUT BUFFER ADDRESS 0 - 2
 CE : CHIP ENABLE
 D0/SER IN : PARALLEL DATA D0/SERIAL DATA
 D1/SER OUT : PARALLEL DATA D1/SERIAL DATA (OUT)
 D2, 3 : PARALLEL DATA D2, 3
 EDGE/LEVEL : EDGE/LEVEL CONTROL
 IN0 - 7 : VIDEO INPUT 0 - 7
 LATCH : LATCH CONTROL
 LOAD : ASYNCHRONOUS CONTROL
 SER/ $\overline{\text{PAR}}$: SERIAL/PARALLEL
 WR : WRITE

OUTPUT

OUT0 - 7 : OUTPUT BUFFERS 0 - 7

