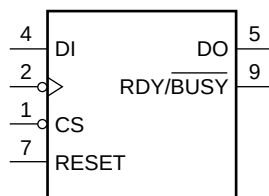
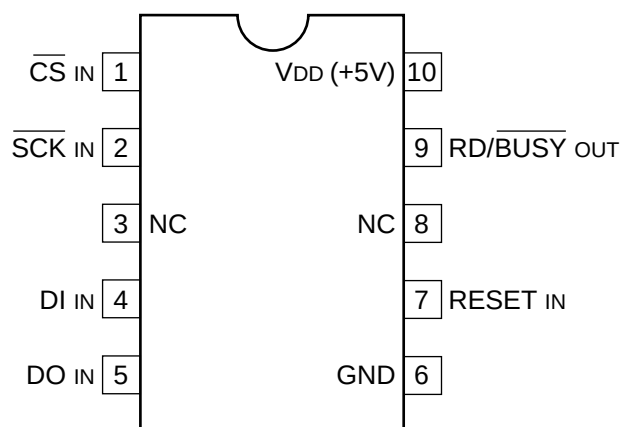

C-MOS 2K (128 × 16) BIT ERASABLE PROM

-TOP VIEW-



CS ; CHIP SELECT INPUT
 DI ; DATA INPUT
 DO ; DATA OUTPUT
 RDY/ $\overline{BUSY}$; BUSY OUTPUT
 RESET ; RESET INPUT
 SCK ; CLOCK INPUT

