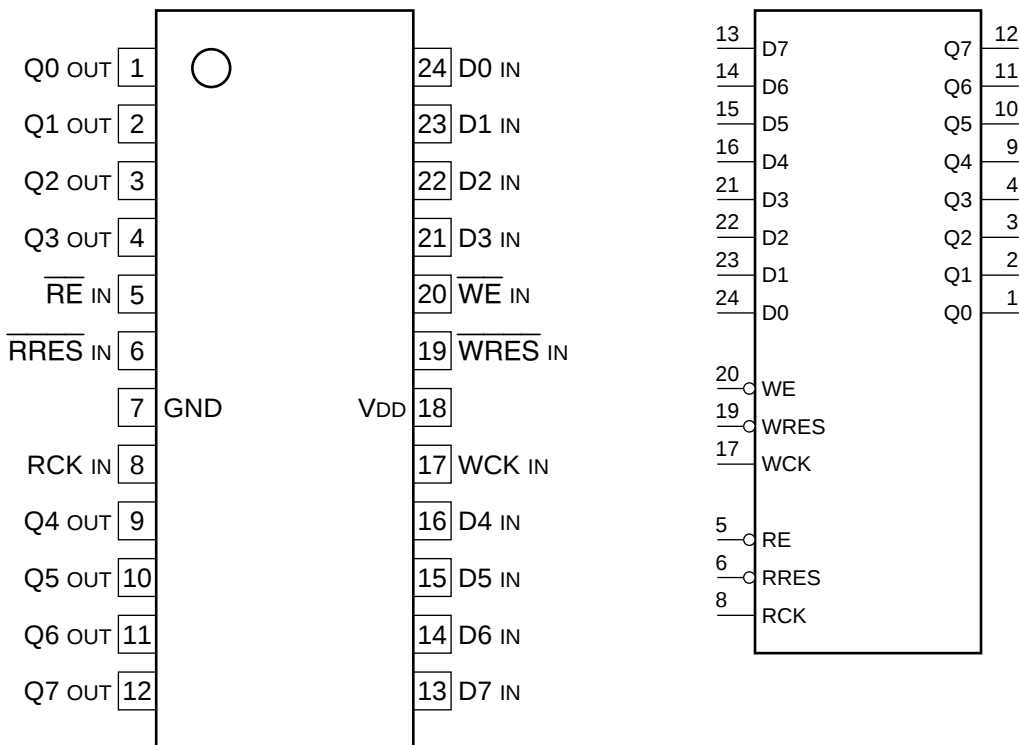


C-MOS 40 M (5,120 × 8)-BIT LINE MEMORY

—TOP VIEW—

**INPUT**

D0 - D7 : DATA

RCK : READ CLOCK

 $\overline{RE}$: READ ENABLE $\overline{RRES}$: READ RESET

WCK : WRITE CLOCK

 $\overline{WE}$: WRITE ENABLE $\overline{WRES}$: WRITE RESET**OUTPUT**

Q0 - Q7 : DATA

