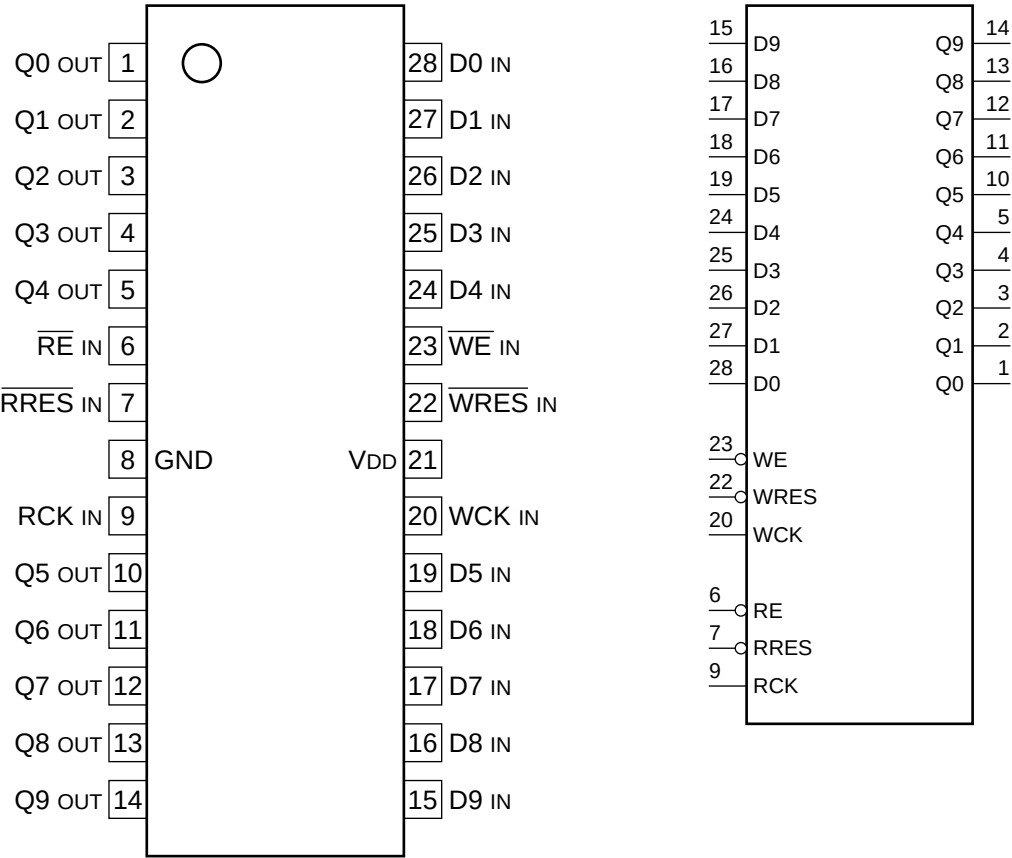


C-MOS 80 M (8,192 × 10)-BIT LINE MEMORY

—TOP VIEW—



- INPUT**
D0 - D9 : DATA
RCK : READ CLOCK
 $\overline{RE}$: READ ENABLE
 $\overline{RRES}$: READ RESET
WCK : WRITE CLOCK
 $\overline{WE}$: WRITE ENABLE
 $\overline{WRES}$: WRITE RESET
- OUTPUT**
Q0 - Q9 : DATA

