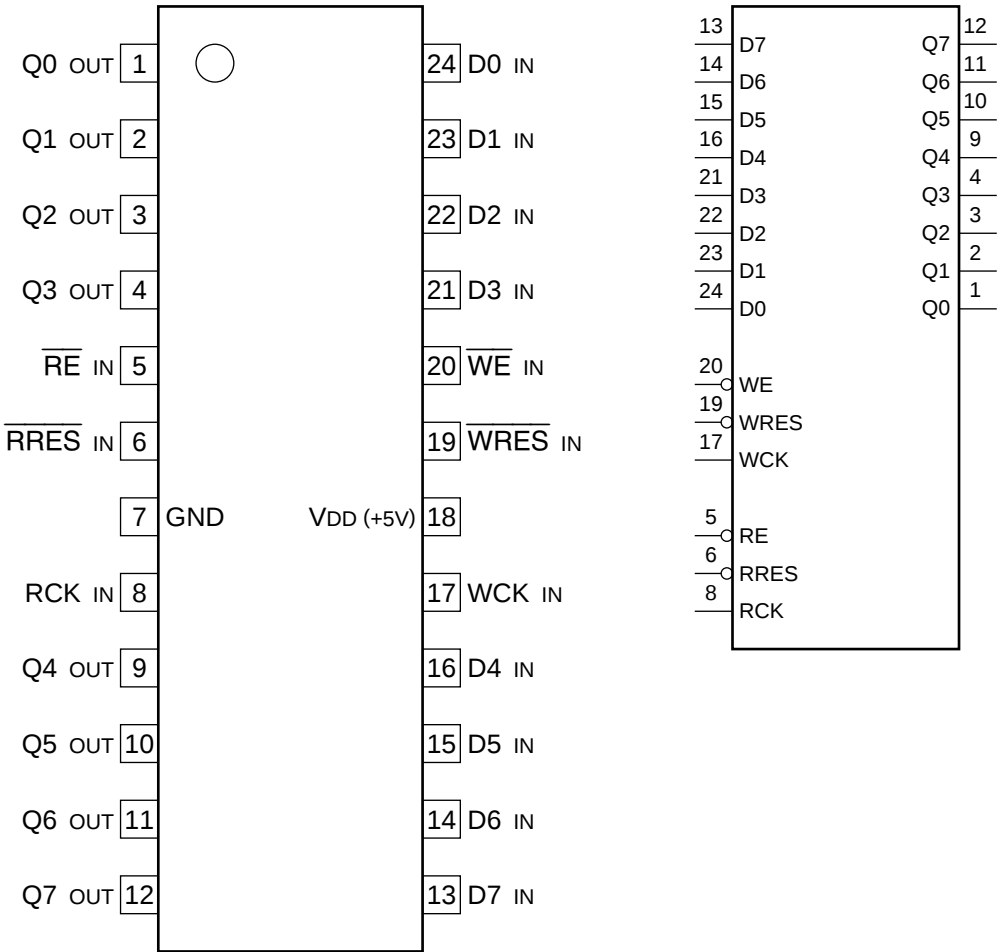

C-MOS 1152 × 8-BIT LINE MEMORY (FIFO)

—TOP VIEW—



INPUT

- D0-D7 ; DATA
- RCK ; READ CLOCK
- $\overline{\text{RE}}$; READ ENABLE
- $\overline{\text{RRES}}$; READ RESET
- WCK ; WRITE CLOCK
- $\overline{\text{WE}}$; WRITE ENABLE
- WRES ; WRITE RESET

OUTPUT

- Q0-Q7 ; DATA

