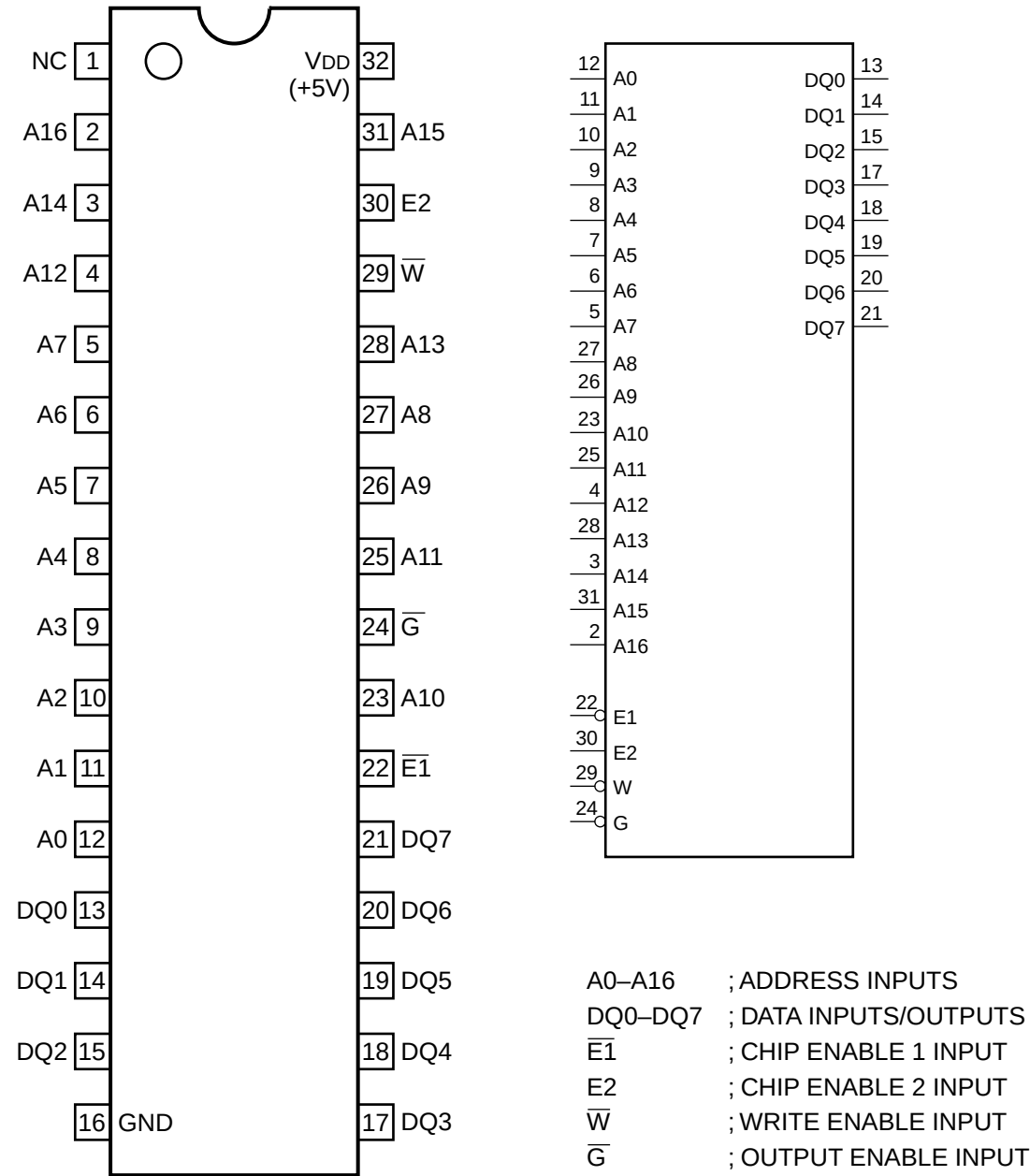

C-MOS 1M (128k × 8) BIT STATIC RAM
-TOP VIEW-



(VDD = +5V)

PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL
1	—	NC	9	I	A3	17	I/O	DQ3	25	I	A11
2	I	A16	10	I	A2	18	I/O	DQ4	26	I	A9
3	I	A14	11	I	A1	19	I/O	DQ5	27	I	A8
4	I	A12	12	I	A0	20	I/O	DQ6	28	I	A13
5	I	A7	13	I/O	DQ0	21	I/O	DQ7	29	I	$\overline{W}$
6	I	A6	14	I/O	DQ1	22	I	$\overline{E1}$	30	I	E2
7	I	A5	15	I/O	DQ2	23	I	A10	31	I	A15
8	I	A4	16	—	GND	24	I	$\overline{G}$	32	—	VDD

