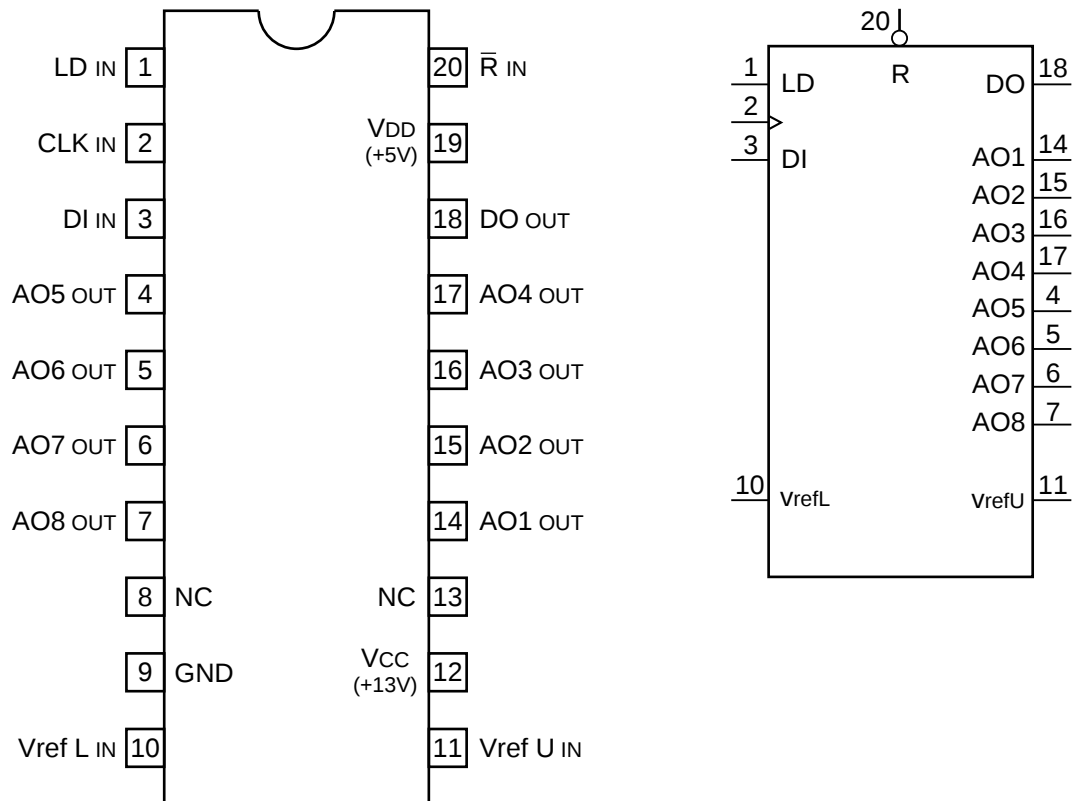

C-MOS 8-BIT 8CHANNEL D/A CONVERTER (WITH BUFFER OPERATIONAL AMPLIFIER) — TOP VIEW —



INPUT

CLK ; SHIFT CLOCK
DI ; SERIAL DATA
LD ; LOAD
 $\bar{R}$; R : LOW LEVEL = AO : Vref L + 1LSB
Vref L ; D/A CONV. LOW SIDE REFERENCE VOLTAGE
Vref U ; D/A CONV. UP SIDE REFERENCE VOLTAGE
Vcc ; ANALOG POWER SUPPLY
VDD ; DIGITAL POWER SUPPLY

OUTPUT

AO1 - AO8 ; D/A CONVERT
DO ; BIT DATA (14-BIT SHIFT REGISTER MSB)

