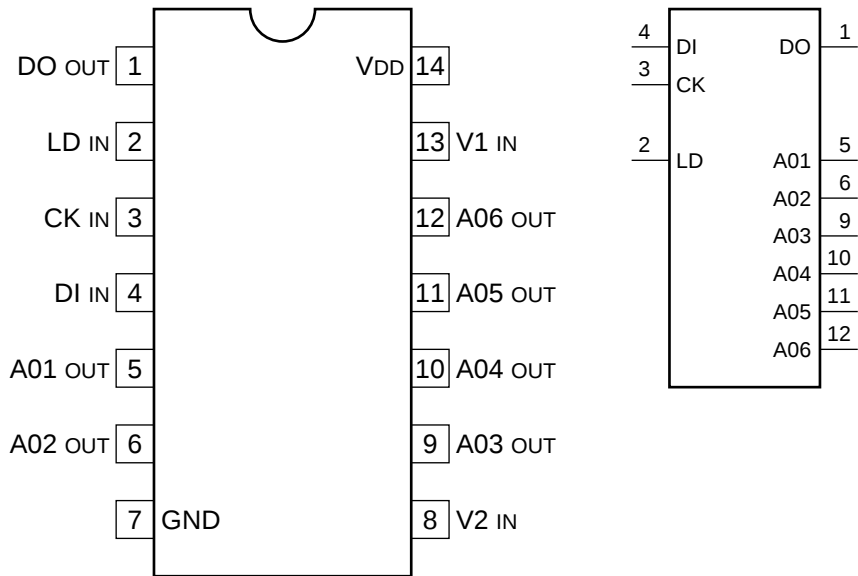


C-MOS 8-BITS 6 CHANNEL D/A CONVERTER

—TOP VIEW—



A01 - A06 ; 8-BITS D/A OUTPUTS

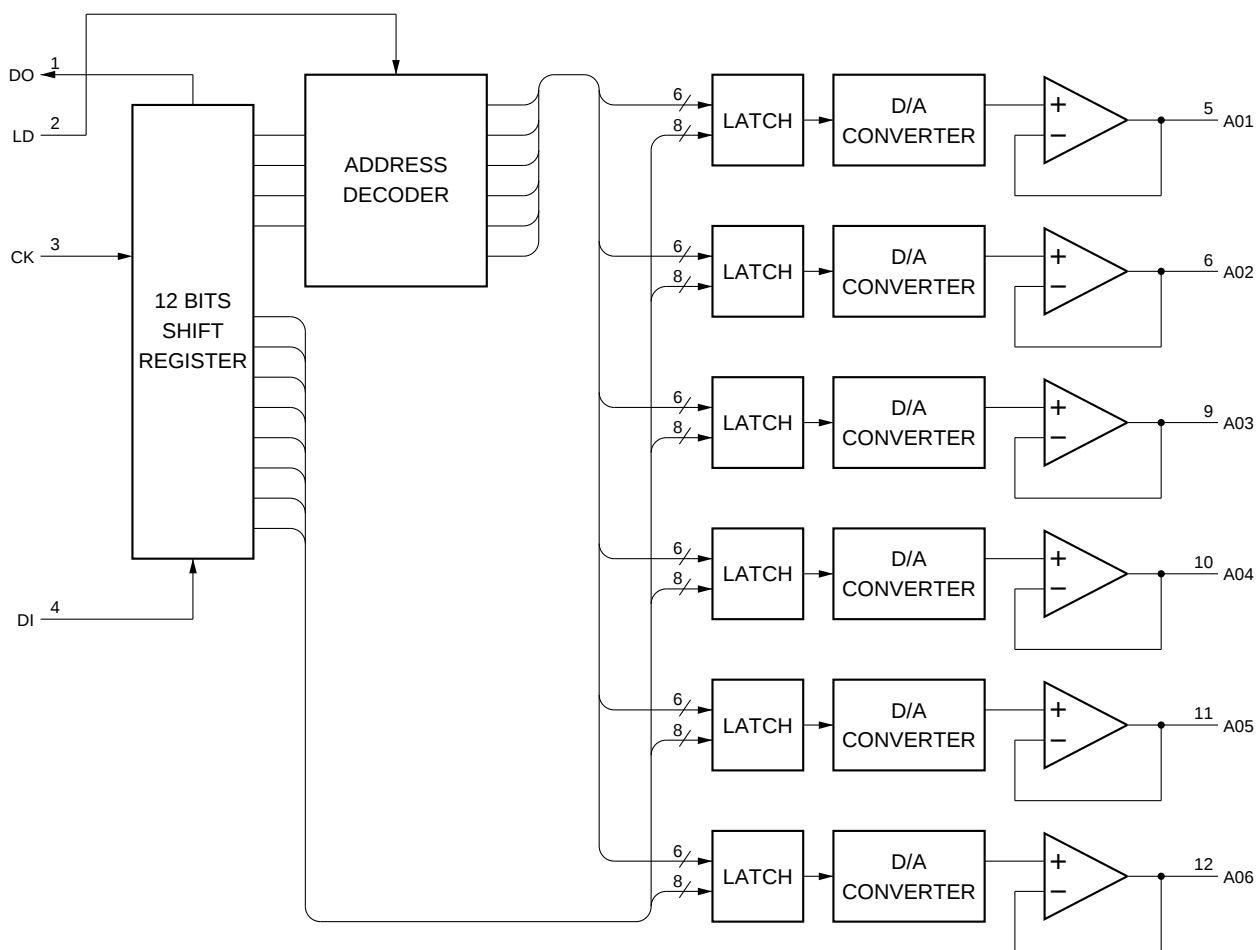
CK ; CLOCK INPUT

DI ; 12-BITS SERIAL DATA INPUT

DO ; BIT DATA OF MSB OF 12-BITS SHIFT REGISTER OUTPUT

LD ; LOAD INPUT

V1 ; REFERENCE VOLTAGE (UPPER) +3.5 to +5 (V_{DD}) VV2 ; REFERENCE VOLTAGE (LOWER) 0 to +1.5 ($V_{DD}-3.5$) V



D0	D1	D2	D3	D4	D5	D6	D7	D/A OUTPUT
0	0	0	0	0	0	0	0	$(V1-V2) / 256 \times 1 + V2$
1	0	0	0	0	0	0	0	$(V1-V2) / 256 \times 2 + V2$
0	1	0	0	0	0	0	0	$(V1-V2) / 256 \times 3 + V2$
1	1	0	0	0	0	0	0	$(V1-V2) / 256 \times 4 + V2$
:	:	:	:	:	:	:	:	:
:	:	:	:	:	:	:	:	:
0	1	1	1	1	1	1	1	$(V1-V2) / 256 \times 255 + V2$
1	1	1	1	1	1	1	1	V1

D8	D9	D10	D11	ADDRESS SELECT
0	0	0	0	X
0	0	0	1	A01
0	0	1	0	A02
0	0	1	1	A03
0	1	0	0	A04
0	1	0	1	A05
0	1	1	0	A06
0	1	1	1	X
1	X	X	X	X

0 ; LOW LEVEL
 1 ; HIGH LEVEL
 X ; DON'T CARE