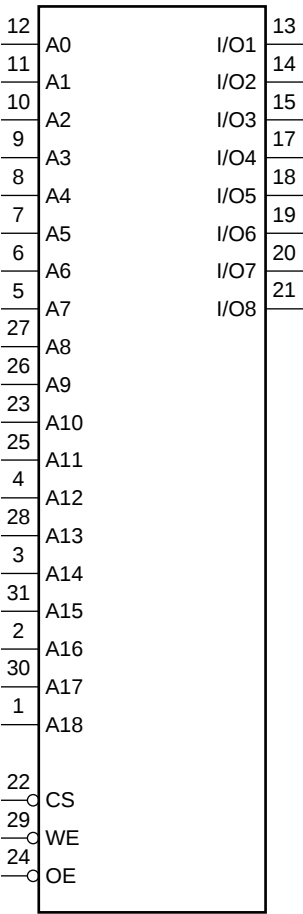
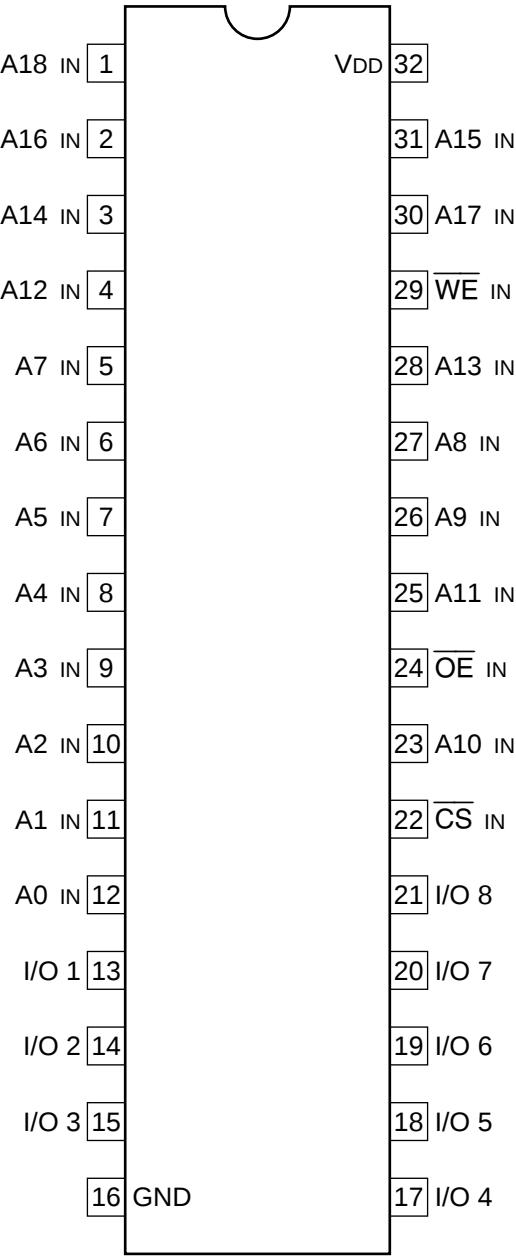


C-MOS 4 M (524,288 × 8)-BIT SRAM
—TOP VIEW—



INPUT
A0 - A18 ; ADDRESS
CS ; CHIP SELECT
OE ; OUTPUT ENABLE
WE ; WRITE ENABLE

INPUT/OUTPUT
I/O1 - I/O8 ; I/O DATA