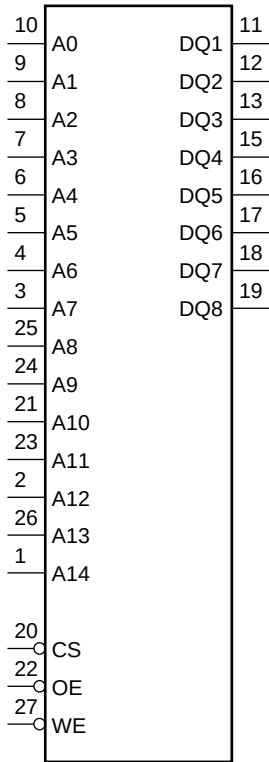
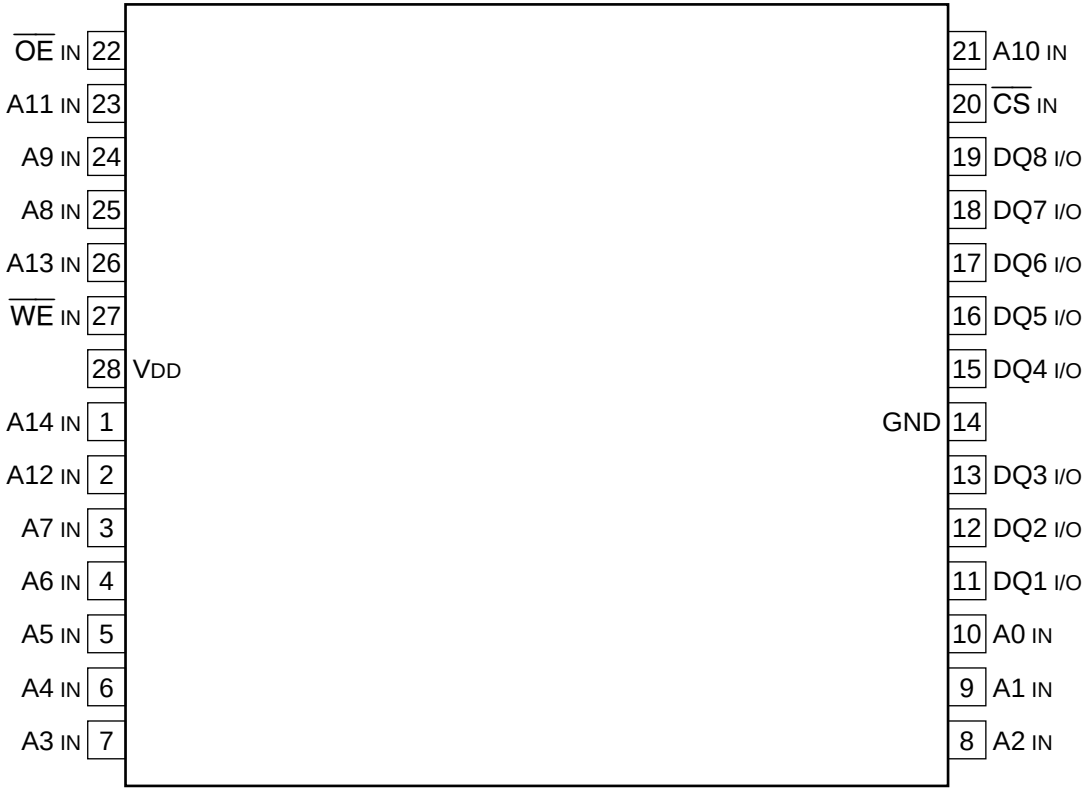


C-MOS 256 K (32,768 × 8)-BIT SRAM

—TOP VIEW—



INPUT

A0 - A14 : ADDRESS

$\overline{CS}$: CHIP SELECT

$\overline{OE}$: OUTPUT ENABLE

$\overline{WE}$: WRITE ENABLE

INPUT/OUTPUT

DQ1 - DQ8 : DATA