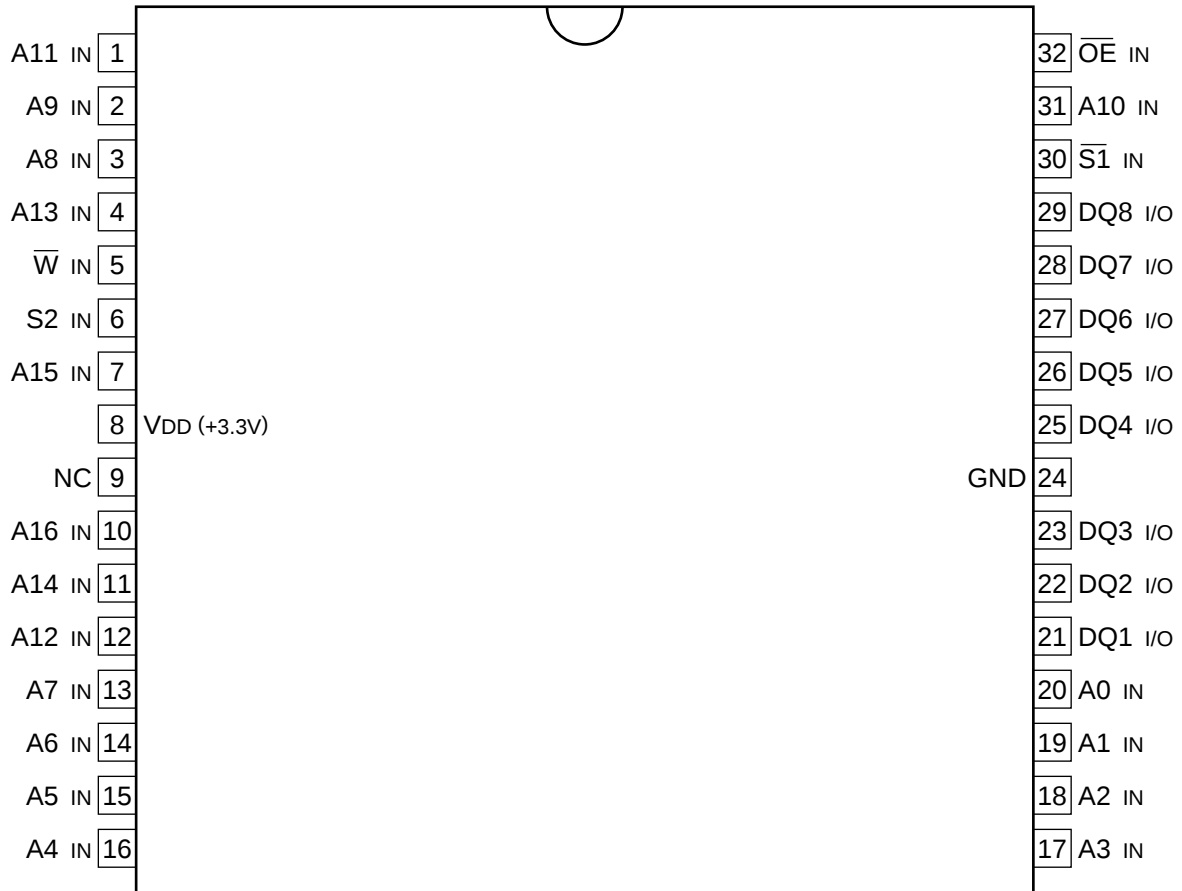

C-MOS STATIC RAM 1M (131,072 8)-BIT

-TOP VIEW-



20	A0	DQ1	21
19	A1	DQ2	22
18	A2	DQ3	23
17	A3	DQ4	25
16	A4	DQ5	26
15	A5	DQ6	27
14	A6	DQ7	28
13	A7	DQ8	29
3	A8		
2	A9		
31	A10		
1	A11		
12	A12		
4	A13		
11	A14		
7	A15		
10	A16		
32	OE		
30	S1		
6	S2		
5	W		

A0-A16 ; ADDRESS INPUTS
 DQ1-DQ8 ; DATA INPUTS/OUTPUTS
 OE ; OUTPUT ENABLE INPUT
 S1, S2 ; CHIP SELECT INPUTS
 W ; WRITE ENABLE INPUT

S1	S2	W	OE	MODE	DQ	Icc
X	0	X	X	NO SELECTION	HI-Z	STANDBY
1	X	X	X	NO SELECTION	HI-Z	STANDBY
0	1	0	X	WRITE	DIN	OPERATION
0	1	1	0	READ	DOUT	OPERATION
0	1	1	1		HI-Z	OPERATION

0 ; LOW LEVEL
 1 ; HIGH LEVEL
 X ; DON'T CARE
 HI-Z; HIGH IMPEDANCE

