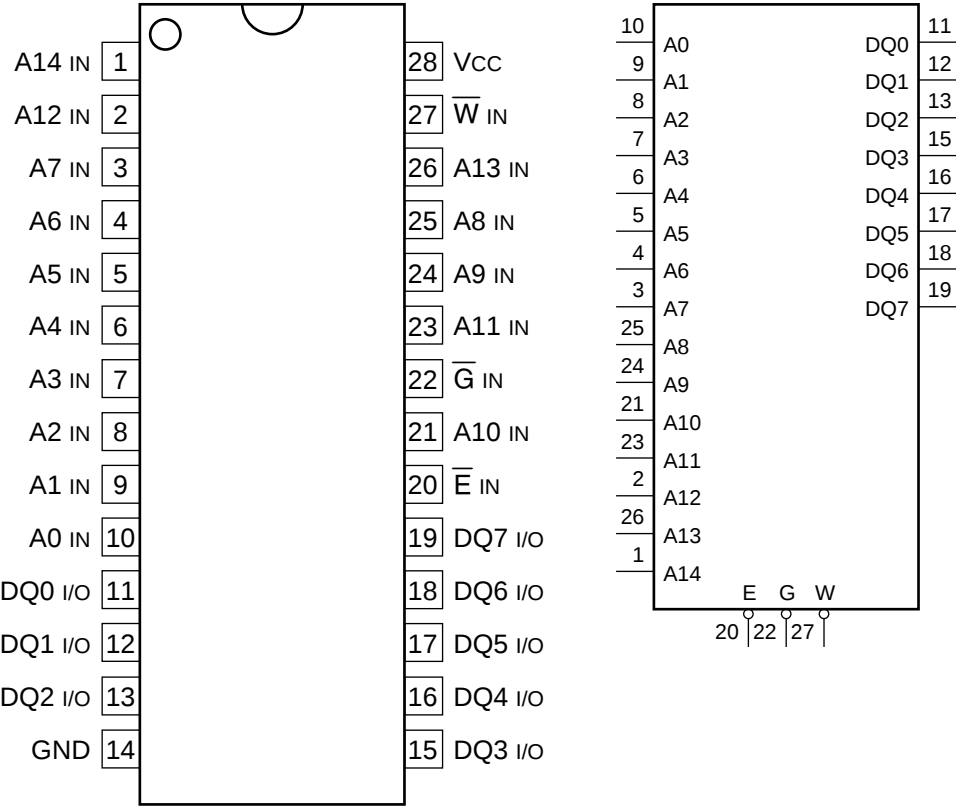


256 K (32,768 × 8)-BIT NON-VOLATILE SRAM WITH BATTERY

—TOP VIEW—



INPUTS

A0 - A14 : ADDRESS

$\overline{E}$: CHIP ENABLE

$\overline{G}$: OUTPUT ENABLE

$\overline{W}$: WRITE ENABLE

INPUTS/OUTPUTS

DQ0 - DQ7 : DATA

MODE	$\overline{E}$	$\overline{G}$	$\overline{W}$	DQ0 - DQ7	POWER
DESELECT	1	x	x	HI-Z	STANDBY
WRITE	0	x	0	DIN	ACTIVE
READ	0	0	1	DOUT	ACTIVE
READ	0	1	1	HI-Z	ACTIVE
DESELECT	x	x	x	HI-Z	CMOS STANDBY
DESELECT	x	x	x	HI-Z	BATTERY BACK-UP MODE

0 : LOW LEVEL

1 : HIGH LEVEL

x : DON'T CARE

HI-Z : HIGH IMPEDANCE

