



M48T128Y M48T128V

3.3V-5V 1 Mbit (128Kb x 8) TIMEKEEPER® SRAM

DATA BRIEFING

- INTEGRATED ULTRA LOW POWER SRAM, REAL TIME CLOCK, POWER-FAIL CONTROL CIRCUIT, BATTERY, AND CRYSTAL
- BCD CODED YEAR, MONTH, DAY, DATE, HOURS, MINUTES, and SECONDS
- AUTOMATIC POWER-FAIL CHIP DESELECT and WRITE PROTECTION
- WRITE PROTECT VOLTAGES (V_{PFD} = Power-fail Deselect Voltage):
 - M48T128Y: $4.1V \leq V_{PFD} \leq 4.5V$
 - M48T128V: $2.7V \leq V_{PFD} \leq 3.0V$
- CONVENTIONAL SRAM OPERATION; UNLIMITED WRITE CYCLES
- SOFTWARE CONTROLLED CLOCK CALIBRATION for HIGH ACCURACY APPLICATIONS
- 10 YEARS of DATA RETENTION and CLOCK OPERATION in the ABSENCE of POWER
- PIN and FUNCTION COMPATIBLE with JEDEC STANDARD 128K X 8 SRAMS
- SELF-CONTAINED BATTERY and CRYSTAL in DIP PACKAGE

DESCRIPTION

The M48T128Y/V TIMEKEEPER RAM is a 128K x 8 non-volatile static RAM and real time clock. The special DIP package provides a fully integrated battery back-up memory and real time clock solution. The M48T128Y/V indirectly replaces industry standard 128K x 8 SRAM.

It also provides the non-volatility of Flash without any requirement for special write timing or limitations on the number of writes that can be performed. The 32 pin 600 mil DIP Hybrid houses a controller chip, SRAM, quartz crystal, and a long life lithium button cell in a single package.

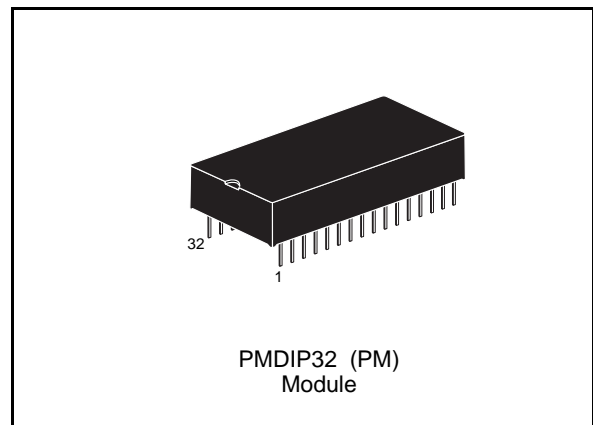
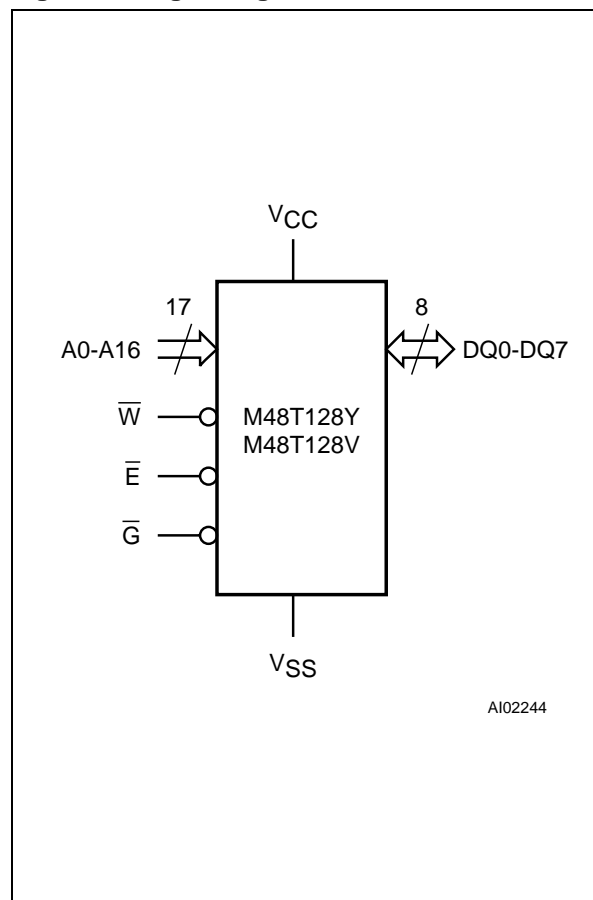
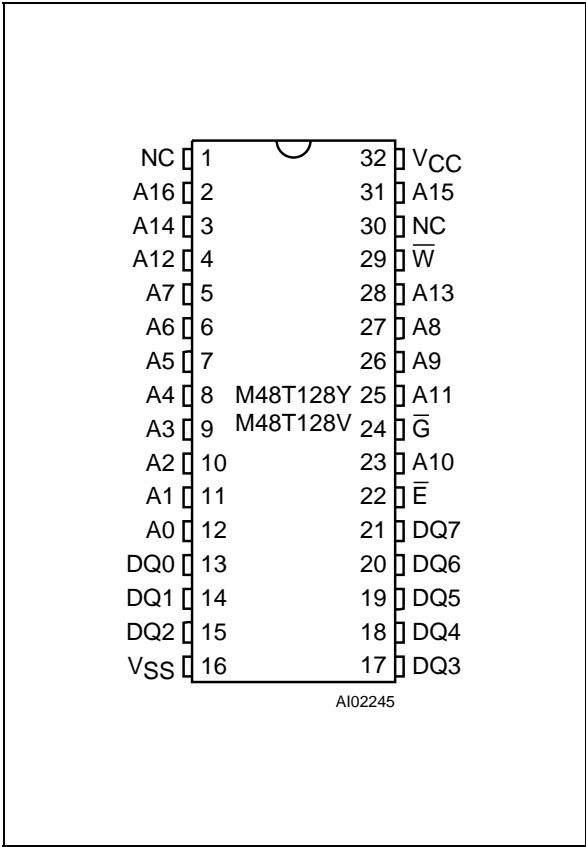


Figure 1. Logic Diagram



M48T128Y, M48T128V

DIP Pin Connections



Note: NC = Not Connected.

Signal Names

A0-A16	Address Inputs
DQ0-DQ7	Data Inputs / Outputs
\overline{E}	Chip Enable Input
\overline{G}	Output Enable Input
\overline{W}	Write Enable Input
V _{CC}	Supply Voltage
V _{SS}	Ground

Ordering Information Scheme

For a list of available options or for further information on any aspect of this device, please contact the STMicroelectronics Sales Office nearest to you.

Example: M48T128Y -70 PC 1

Supply Voltage and Write Protect Voltage	
Y	V _{CC} = 4.5V to 5.5V V _{PFD} = 4.1V to 4.5V
V	V _{CC} = 3.0V to 3.6V V _{PFD} = 2.7V to 3.0V
Speed	
-70	70ns
-85	85ns
Package	
PM	PMDIP32
Temperature Range	
1	0 to 70 °C