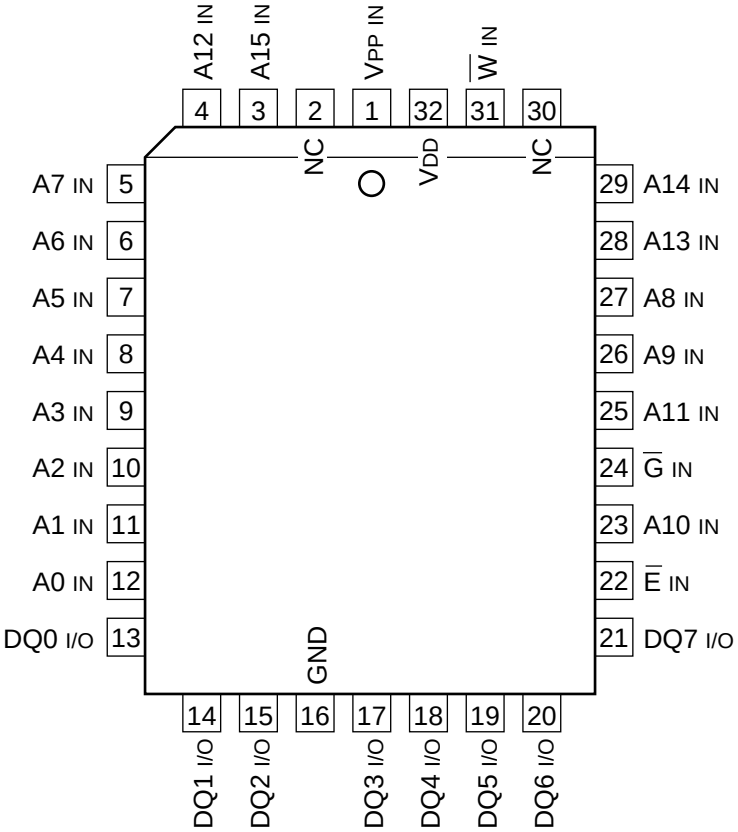
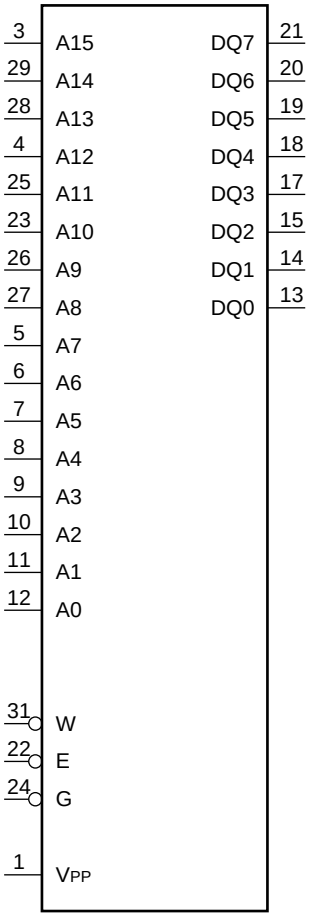
# C-MOS 512 K (65, 536 × 8)-BIT FLASH MEMORY

—TOP VIEW—




**INPUT**

A0 - A15 : ADDRESS
$\overline{E}$ : CHIP ENABLE
$\overline{G}$ : OUTPUT ENABLE
VPP : PROGRAM SUPPLY
$\overline{W}$ : WRITE ENABLE

**INPUT/OUTPUT**

DQ0 - DQ7 : DATA