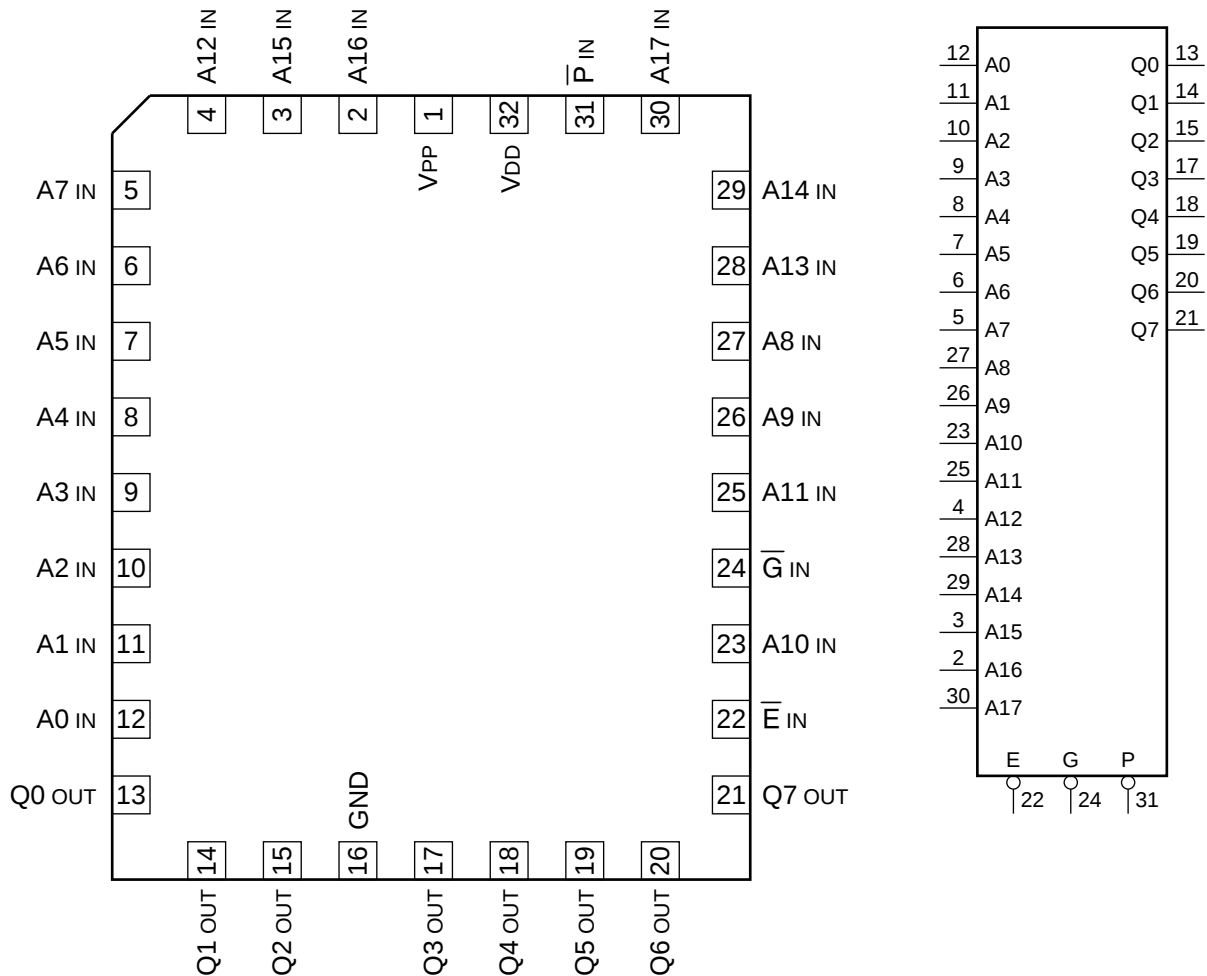


C-MOS 2 M (256 × 8)-BIT UV EPROM AND OTP ROM

—TOP VIEW—

**INPUTS**

A0 - A17 : ADDRESS

 $\bar{E}$: CHIP ENABLE $\bar{G}$: OUTPUT ENABLE $\bar{P}$: PROGRAM**OUTPUT**

Q0 - Q7 : DATA

MODE	E	G	$\bar{E}$	$\bar{G}$	$\bar{P}$	Q0 - Q7
READ	0	0	x	x	x	DATA OUT
OUTPUT DISABLE	0	1	x	x	x	HI-Z
PROGRAM	0	1		x	VPP	DATA IN
VERIFY	0	0	1	x	VPP	DATA OUT
PROGRAM INHIBIT	1	x	x	x	VPP	HI-Z
STANDBY	1	x	x	x	x	HI-Z
ELECTRONIC SIGNATURE	0	0	1	V _{ID}	V _{DD}	CODES

0 : LOW LEVEL

1 : HIGH LEVEL

x : DON'T CARE

HI-Z : HIGH IMPEDANCE