

64 Kbit (8Kb x 8) NMOS UV EPROM

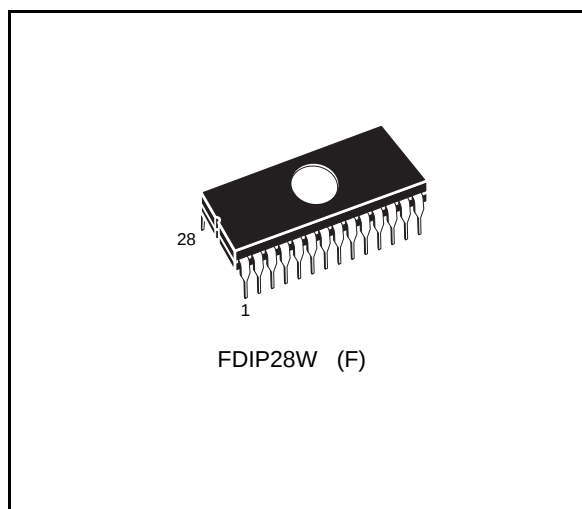
DATA BRIEFING

- FAST ACCESS TIME: 180ns
- EXTENDED TEMPERATURE RANGE
- SINGLE 5V SUPPLY VOLTAGE
- LOW STANDBY CURRENT: 35mA max
- TTL COMPATIBLE DURING READ and PROGRAM
- FAST PROGRAMMING ALGORITHM
- ELECTRONIC SIGNATURE
- PROGRAMMING VOLTAGE: 12V

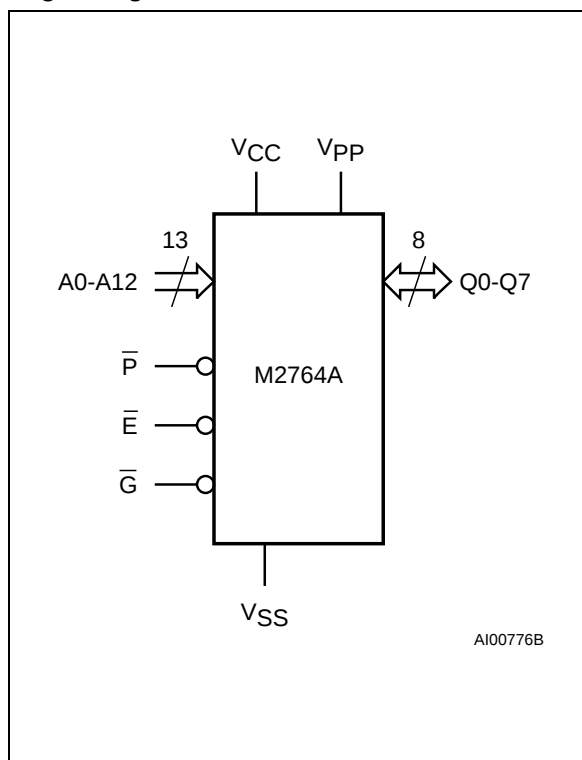
DESCRIPTION

The M2764A is a 65,536 bit UV erasable and electrically programmable memory EPROM. It is organized as 8,192 words by 8 bits.

The M27C64A is housed in a 28 pin Window Ceramic Frit-Seal Dual-in-Line package. The transparent lid allows the user to expose the chip to ultraviolet light to erase the bit pattern. A new pattern can then be written to the device by following the programming procedure.



Logic Diagram

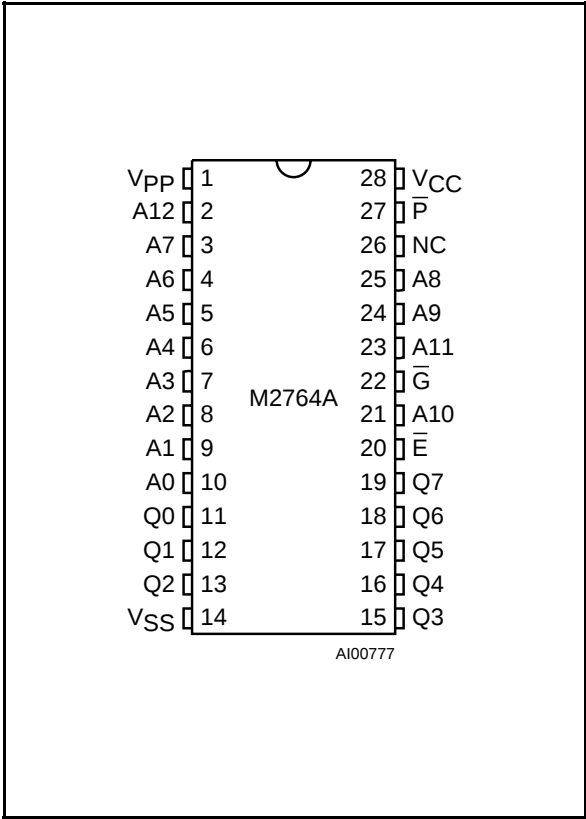


Signal Names

A0-A12	Address Inputs
Q0-Q7	Data Outputs
$\bar{E}$	Chip Enable
$\bar{G}$	Output Enable
$\bar{P}$	Program
V_{PP}	Program Supply
V_{CC}	Supply Voltage
V_{SS}	Ground

M2764A

DIP Pin Connections



Ordering Information Scheme

For a list of available options or for further information on any aspect of this device, please contact the SGS-THOMSON Sales Office nearest to you.

Example: M2764A -1 F 1

Speed and V _{CC} Tolerance	
-1	180ns, 5V±5%
-2	200ns, 5V±5%
blank	250ns, 5V±5%
-3	300ns, 5V±5%
-4	450ns, 5V±5%
-20	200ns, 5V±10%
-25	250ns, 5V±10%
Package	
F	FDIP28W
Temp. Range	
1	0 to 70 °C
6	-40 to 85 °C