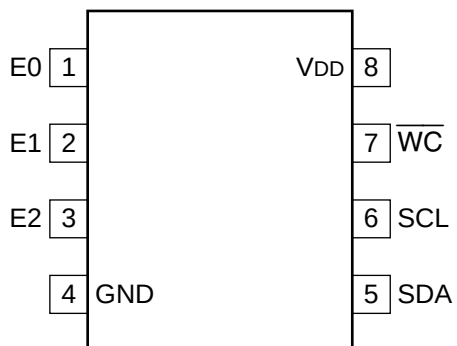


C-MOS I²C BUS EEPROM

—TOP VIEW—

**INPUT**

E0 - E2 : CHIP ENABLE

 $\overline{SCL}$: SERIAL CLOCK $\overline{WC}$: WRITE CONTROL**OUTPUT**

SDA : SERIAL DATA ADDRESS